

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL
GATES AND INVERTERS

• **POSITIVES – NAND GATES AND INVERTERS**

DESCRIÇÃO	TIPO
Hex Invertes	74LS04
Quadruple 2 – Inputs Gates	74LS00
Triple 3 – Inputs Gates	74LS10
Dual 4 – Inputs Gates	74LS20
8 – Inputs Gates	74LS30
13 – Inputs Gates	74LS133

• **POSITIVES – NAND GATES AND INVERTERS WITH OPEN – COLLECTOR OUTPUTS**

DESCRIÇÃO	TIPO
Hex Invertes	74LS05
Quadruple 2 – Inputs Gates	74LS01
	74LS03
Triple 3 – Inputs Gates	74LS12
Dual 4 – Inputs Gates	74LS22

• **POSITIVES – AND GATES**

DESCRIÇÃO	TIPO
Quadruple 2 – Inputs Gates	74LS08
Triple 3 – Inputs Gates	74LS11
Dual 4 – Inputs Gates	74LS21

• **POSITIVES – AND GATES WITH OPEN – COLLECTOR OUTPUTS**

DESCRIÇÃO	TIPO
Quadruple 2 – Inputs Gates	74LS09
Triple 3 – Inputs Gates	74LS15

• **POSITIVES – OR GATES**

DESCRIÇÃO	TIPO
Quadruple 2 – Inputs Gates	74LS32

• **POSITIVES – NOR GATES**

DESCRIÇÃO	TIPO
Quadruple 2 – Inputs Gates	74LS02
Triple 3 – Inputs Gates	74LS27
Dual 4 – Inputs Gates with Strobe	74LS25

• **SCHMITT – TRIGGER POSITIVES – NAND GATES AND INVERTERS**

DESCRIÇÃO	TIPO
Hex Invertes	74LS14
Dual 4 – Inputs Positive – NAND	74LS13

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL
GATES, EXPANDERS, BUFFERS, DRIVERS AND
TRANSCEIVERS

• **AND – OR – INVERTERS GATES**

DESCRIÇÃO	TIPO
2 – Wide 4 – Input	74LS55
4 – Wide 2–2–3–2 – Input	74LS54
4 – Wide 2 – Input	
4 – Wide 2–3–3–2 – Input	
Dual 2 – Wide 2 – Input	74LS51

• **EXPANDERS GATES**

DESCRIÇÃO	TIPO
4 – Wide AND – OR	74LS53
2 – Wide AND – OR – INVERT	74LS55
Dual 2 – Wide AND – OR – INVERT	74LS50

• **BUFFER AND INTERFACE GATES WITH OPEN – COLLECTOR OUTPUTS**

DESCRIÇÃO	TIPO
Hex	74LS07
	74LS17
Hex Inverter	74LS06
Quad 2 – Input Positive – NAND	74LS26
	74LS38
Quad 2 – Input Positive – NOR	74LS33

• **GATES, EXPANDERS, BUFFERS, DRIVERS AND TRANSCEIVERS**

DESCRIÇÃO	TIPO
Noinverting Octal Buffers/Drivers	74LS241
	74LS244
Inverting Octal Buffers/Drivers	74LS240
Octal Transceivers	74LS245
Noinverting Hex Buffers/Drivers	74LS365
	74LS367
Noinverting Hex Buffers/Drivers	74LS366
	74LS368
Quad Buffers/Drivers with Independent Output Controls	74LS125
	74LS126
Noninverting Quad Transceivers	74LS243
Inverting Quad Transceivers	74LS242

**“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL
BUFFERS, DRIVERS, TRANSCEIVERS AND CLOCK**

GENERATORS

• **BUFFERS, CLOCK/MEMORY DRIVERS**

DESCRIÇÃO	TIPO
Quad 2 – Input Positive NAND	74LS37
Quad 2 – Input Positive NOR	74LS28
Quad 2 – Input Positive NAND	74LS40

• **OCTAL BI-/TRI-DIRECTIONAL BUS TRANSCEIVERS**

DESCRIÇÃO	TIPO
12 mA, 24 mA, 48 mA, 64 mA sink, True Outputs (3-state)	74LS245

FLIP-FLOP

• **DUAL AND SINGLE FLIP-FLOPS**

DESCRIÇÃO	TIPO
Dual J – K Edge Triggered	74LS73
	74LS76
	74LS78
	74LS107
	74LS109
	74LS112
	74LS114
Dual Pulse Triggered	74LS73
	74LS76
	74LS78
	74LS107
Dual J – K with Data Lockout	74111
Dual D Type	74LS74

• **QUAD AND HEX FLIP – FLOPS**

DESCRIÇÃO	TIPO
D Type	74LS174
	74LS378
	74LS171
	74LS175
	74LS379

• **OCTAL, 9-BIT, AND 10-BIT D-TYPE FLIP – FLOPS**

DESCRIÇÃO	TIPO
True Data with Clear	74LS273
	74LS364
True with Enable	74LS377

LATCHES AND MULTIVIBRATORS

• *QUAD LATCHES*

DESCRIÇÃO	TIPO
Dual 2 –Bit Transparent	74LS75
	74LS375
S – R	74LS279

• *RETRIGGERABLE MONOSTABLE MULTIVIBRATORS*

DESCRIÇÃO	TIPO
Single	74LS122
Dual	74LS123

• *OCTAL LATCHES*

DESCRIÇÃO	TIPO
Addressable	74LS259

• *MONOSTABLE MULTIVIBRATORS WITH SCHMITT-TRIGGER*

DESCRIÇÃO	TIPO
Single	74LS121
Dual	74LS221

REGISTERS

• *SHIFT REGISTERS*

DESCRIÇÃO	TIPO
Parallel In, Parallel Out, Bidirectional	74LS198
	74LS299
	74LS323
	74LS194
Parallel In, Parallel Out	74LS95
	74LS195
	74LS295
	74LS395
Serial In, Parallel Out	74LS164
Parallel In, Serial Out	74LS165
	74LS166
Serial In, Serial Out	74LS91

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

• **REGISTER FILES**

DESCRIÇÃO	TIPO
4 Words × 4 Bits	74LS170
	74LS670

• **OTHER REGISTERS**

DESCRIÇÃO	TIPO
Quadruple Multiplexers with Storage	74LS298
	74LS399
8 – Bit Universal Shift Registers	74LS299
Quadruple Bus Buffer Registers	74LS173

COUNTERS

• **SYNCHRONOUS COUNTERS – POSITIVE-EDGE TRIGGERED**

DESCRIÇÃO	TIPO
Decade	74LS160
	74LS162
	74LS194
Decade Up/Down	74LS190
	74LS192
4-Bit Binary	74LS161
	74LS163
4-Bit Binary Decade Up/Down	74LS169
	74LS191
	74LS193

• **ASYNCHRONOUS COUNTERS (RIPPLE CLOCK) – NEGATIVE-EDGE TRIGGERED**

DESCRIÇÃO	TIPO
Decode	74LS90
4-Bit Binary	74LS93
	74LS197
	74LS293
Divide by 12	74LS92
Dual Decode	74LS390
	74LS490
Dual 4-Bit Binary	74LS393

• **8-BIT BINARY COUNTERS WITH REGISTERS**

DESCRIÇÃO	TIPO
Parallel Register Outputs	74LS590

DECODERS, ENCODERS, DATA SELECTOR/MULTIPLEXERS AND SHIFTERS

• DATA SELECTORS MULTIPLEXERS

DESCRIÇÃO	TIPO
16 – to – 1	74LS150
8 – to – 1	74LS151
	74LS251
Dual 4 – to – 1	74LS153
	74LS253
	74LS352
	74LS353
Octal 2 – to – 1 with Storage	74LS298
	74LS399
Quad 2 – to – 1	74LS157
	74LS158
	74LS257
	74LS258

• DECODERS/DEMULTIPLEXERS

DESCRIÇÃO	TIPO
4 – to – 16	74LS154
	74159
4 – to – 10 BCD – to – Decimal	74LS42
4 – to – 10 Excess – 3 – to – Decimal	74LS43
4 – to – 10 Excess 3 Gray – to – Decimal	74LS44
3 – to – 8	74LS138
Dual 2 – 4	74LS139
	74LS155
	74LS156

• PRIORITY ENCODERS/REGISTERS

DESCRIÇÃO	TIPO
Full BCD	74LS147
Cascadable Octal	74LS148
Cascadable Octal with 3 – state Outputs	74LS348

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL
DISPLAYS DECODERS/ENCODERS,
MEMORY/MICROPROCESSOR CONTROLLERS AND
VOLTAGE – CONTROLLED OSCILLATORS

• ***OPEN-COLLECTOR DISPLAY DECODERS/DRIVERS***

DESCRIÇÃO	TIPO
BCD to Decimal	74LS45
	74LS145
BCD – to – Seven – Segment	74LS47
	74LS48
	74LS49
	74LS247

• ***OPEN-COLLECTOR DISPLAY DECODERS/DRIVERS WITH COUNTERS/LATCH***

DESCRIÇÃO	TIPO
BCD Counter/4-Bit Latch/BCD-to-Seven-Segment Decoder/Latch Driver	74LS143

• ***VOLTAGE – CONTROLLED OSCILLATORS***

DESCRIÇÃO	TIPO
Dual	74LS124
	74LS629

• ***CLOCK GENERATOR CIRCUITS***

DESCRIÇÃO	TIPO
Dual VCO	74LS124

COMPARATORS AND ERROR DETECTION CIRCUITS

• ***PARITY GENERATORS/CHECKERS, ERROR DETECTION AND CORRECTION CIRCUITS***

DESCRIÇÃO	TIPO
Odd Even Parity Generators Checkers	74LS180
	74LS280

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL
ARITHMETIC CIRCUITS AND PROCESSOR ELEMENTS

• **PARALLEL BINARY ADDERS**

DESCRIÇÃO	TIPO
4 Bit	74LS83
	74LS283

• **ACCUMULATORS, ARITHMETIC LOGIC UNITS, LOOK-AHEAD CARRY GENERATORS**

DESCRIÇÃO	TIPO
4 – Bit Arithmetic Logic Units/ Function Generators	74LS181
	74LS381

• **MULTIPLIERS**

DESCRIÇÃO	TIPO
2-Bit-by-4-Bit-Parallel Binary Multipliers	74LS261

• **OTHER ARITHMETIC OPERATORS**

DESCRIÇÃO	TIPO
Quad 2-Inputs Exclusive –OR Gates with Totem-Pole Outputs	74LS86
	74LS386
Quad 2-Inputs Exclusive –OR Gates with Open Collector Outputs	74LS136
Quad 2-Inputs Exclusive –NOR Gates with Open Collector Outputs	74LS266

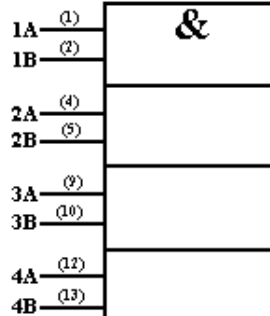
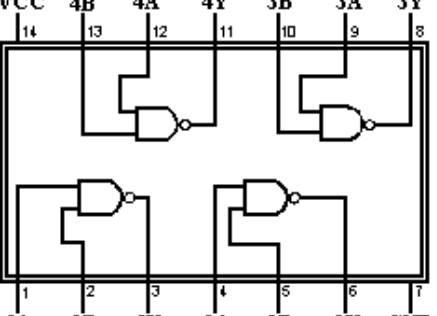
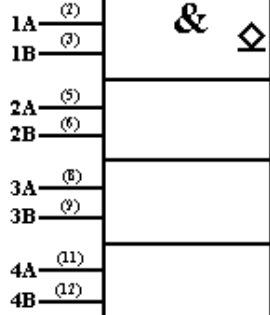
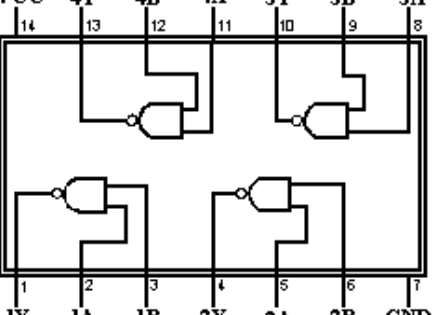
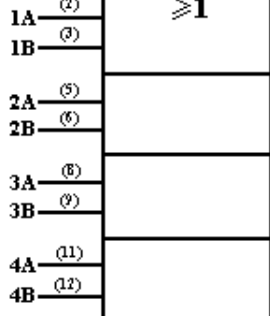
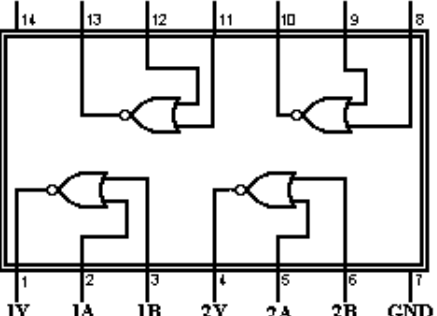
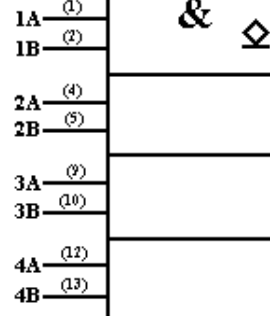
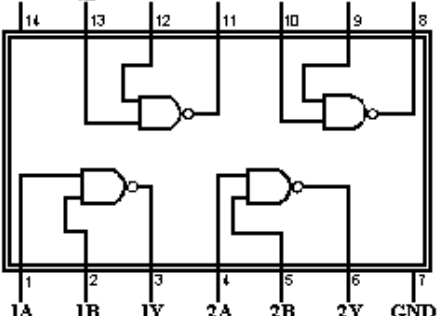
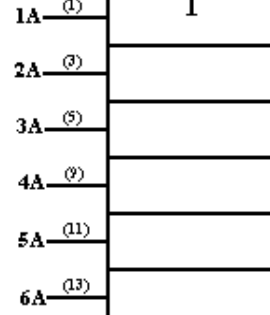
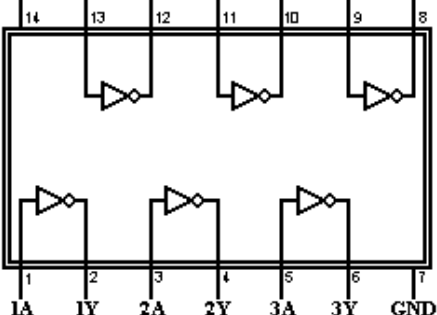
MEMORIES

• **RANDOM-ACCESS READ-WRITE MEMORIES (RAM'S)**

DESCRIÇÃO	TIPO
16 – Bit Register File	74LS170
	74LS670

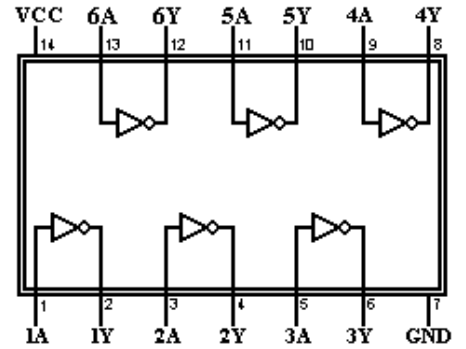
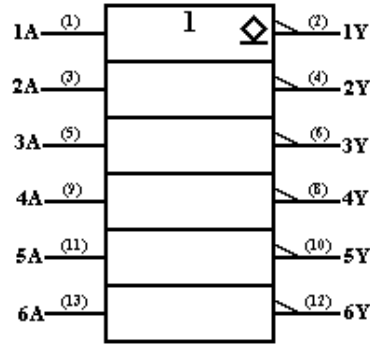
DATASHEET

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

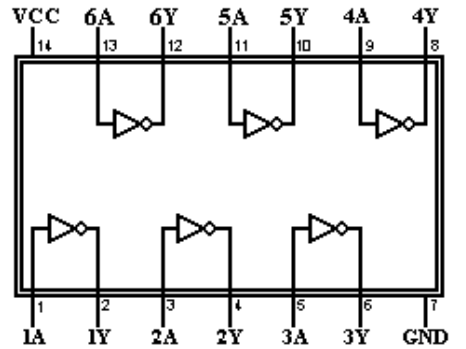
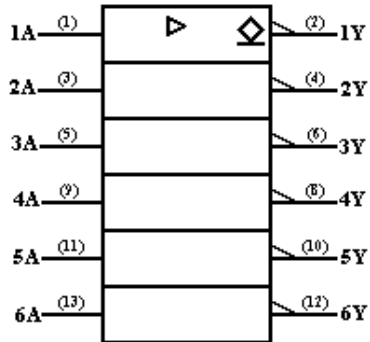
<p>74LS00 Quadruple 2 – Input Gates Positive – Nands Gates</p>		
<p>74LS01 Quadruple 2 – Input Positive – Nands Gates With open – collectors outputs</p>		
<p>74LS02 Quadruple 2 – Inputs Positive – Nor Gates</p>		
<p>74LS03 Quadruple 2 – Inputs Positive – Nands Gates With open – collectors outputs</p>		
<p>74LS04 Hex Invertes</p>		

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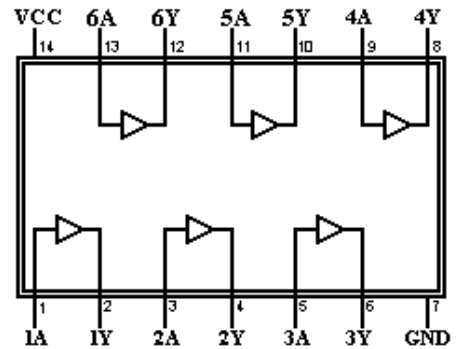
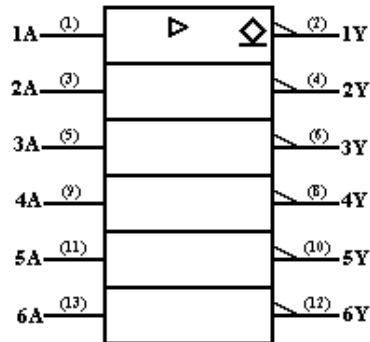
74LS05
Hex Invertes
With Open-Collector Outputs



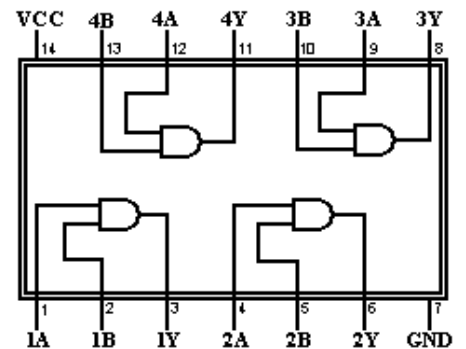
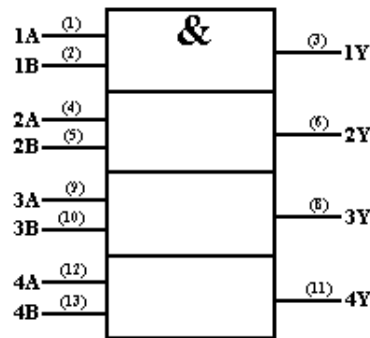
74LS06
Hex Invertes
With Open-Collector
High-Voltage Outputs



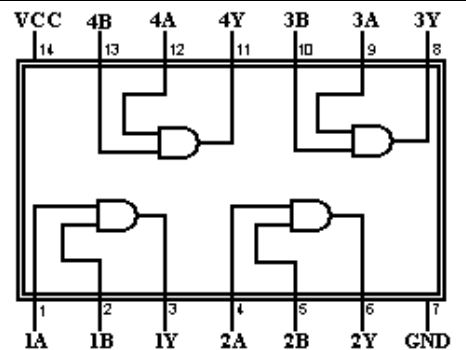
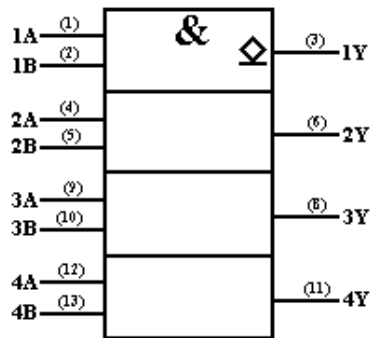
74LS07
Hex Buffers/Drivers
With Open-Collector
High-Voltage Outputs



74LS08
Quadruple 2-Input
Positive – And Gates



74LS09
Quadruple 2-Input
Positive – And Gates
With Open-Collector Outputs



“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

<p>74LS10 Triple 3-Input Positive – Nand Gates</p>		
<p>74LS11 Triple 3-Input Positive – And Gates</p>		
<p>74LS12 Triple 3-Input Positive – Nand Gates With Open-Collector Outputs</p>		
<p>74LS13 Dual 4-Input Positive-Nand Schmitt Triggers</p>		
<p>74LS14 Hex Schmitt-Trigger Inverters</p>		

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

<p>74LS15 Triple 3-Input Positive – And Gates With Open-Collector Outputs</p>		
<p>74LS17 Hex Buffers/Drivers With Open-Collector High-Voltage Outputs</p>		
<p>74LS20 Dual 4-Input Positive-Nand Gates</p>		
<p>74LS21 Dual 4-Input Positive-And Gates</p>		
<p>74LS22 Dual 4-Input Positive – Nand Gates With Open-Collector Outputs</p>		

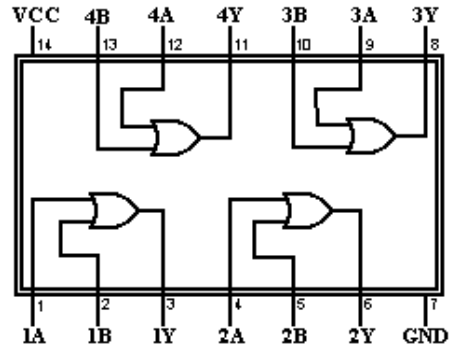
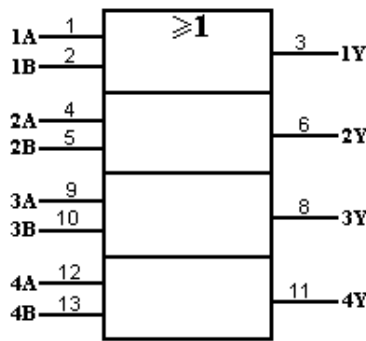
“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

<p>74LS25 Dual 4-Input Positive – Nor Gates With Strobe</p>		
<p>74LS26 Quadruple 2 – Input High – Voltage Interface Positive – Nand Gates</p>		
<p>74LS27 Triple 3 – Input Positive – Nor Gates</p>		
<p>74LS28 Quadruple 2 – Input Positive – Not Buffers</p>		
<p>74LS30 8 – Input Positive – Nand Gates</p>		

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

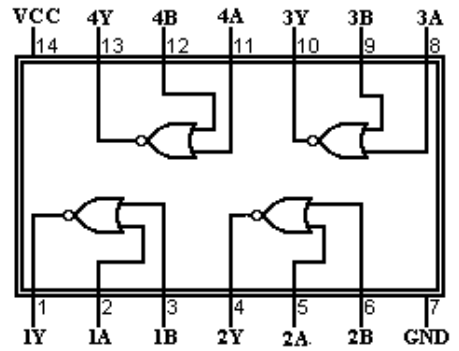
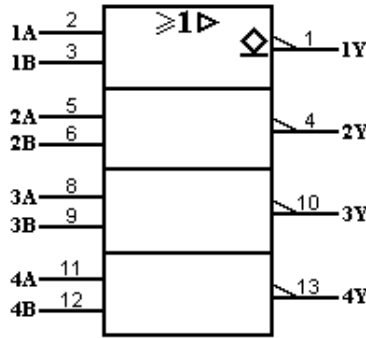
74LS32

**Quadruple 2 – Input
Positive – Or Gates**



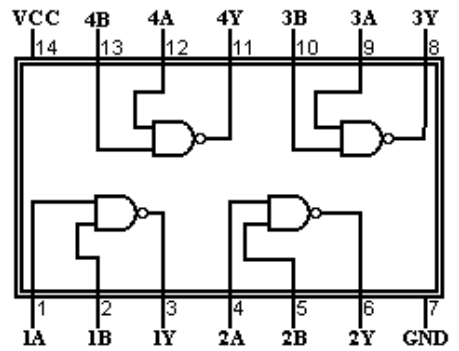
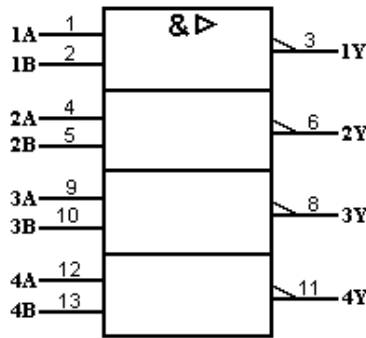
74LS33

**Quadruple 2 – Input
Positive – Nor Buffers
With Open – Collector Outputs**



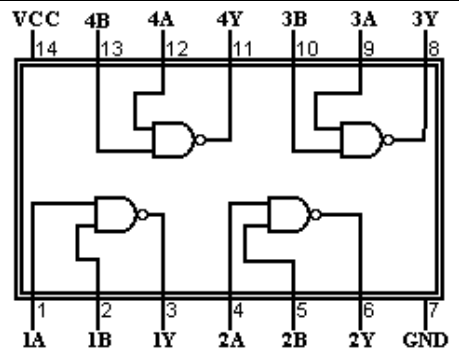
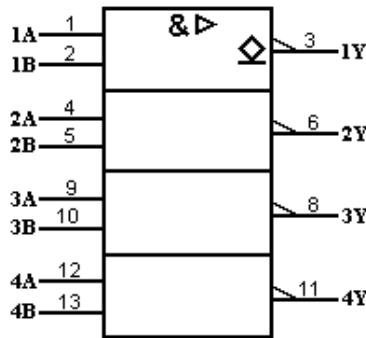
74LS37

**Quadruple 2 – Input
Positive – Nand Gates**



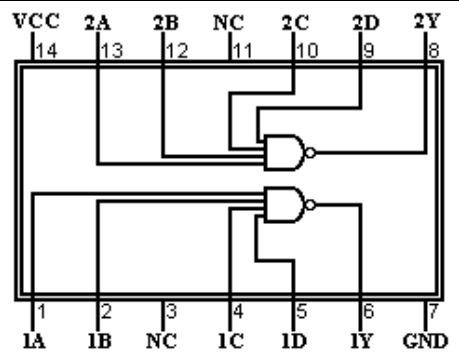
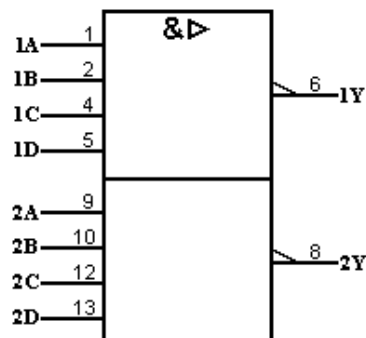
74LS38

**Quadruple 2 – Input
Positive – Nand Buffers
With Open – Collector Outputs**

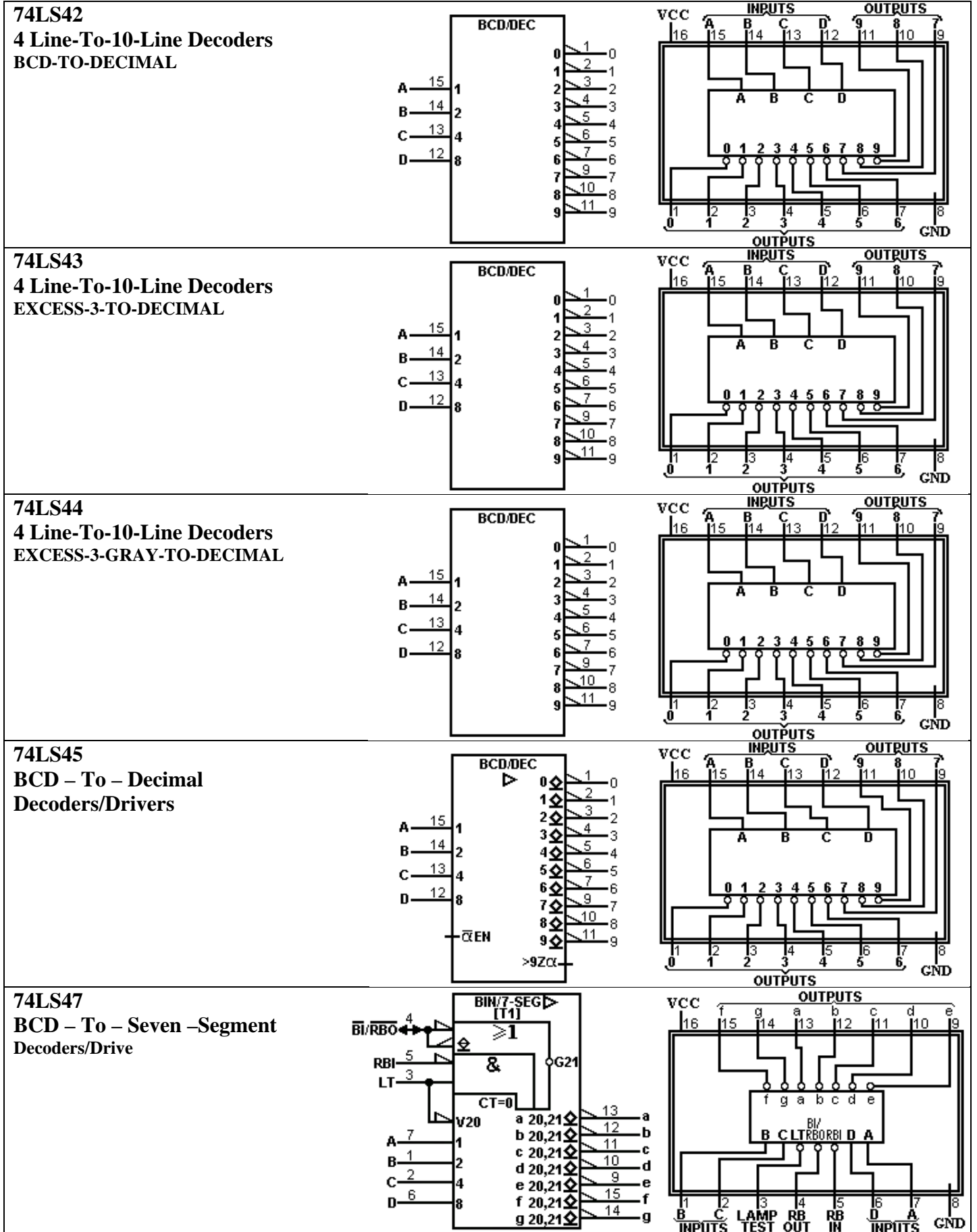


74LS40

**Dual 4 – Input
Positive – Nand Buffers**

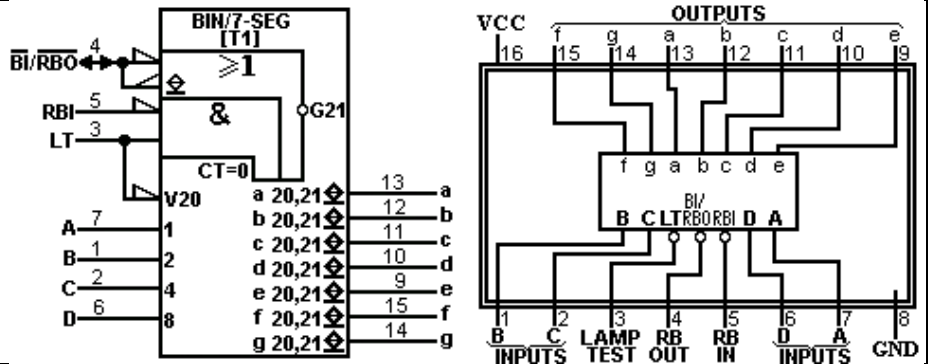


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

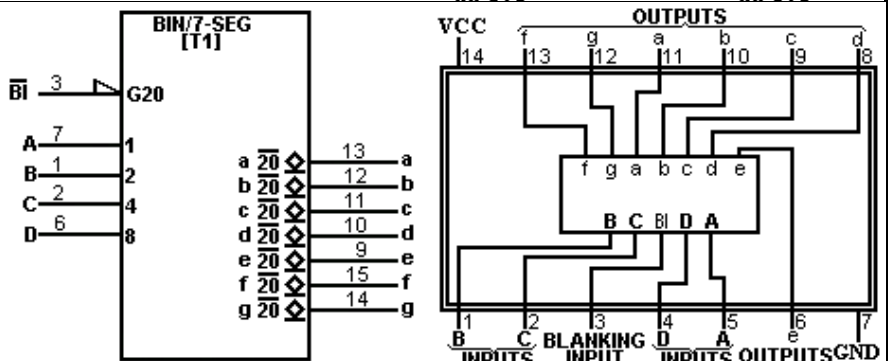


"DATA SHEETS" DE COMPONENTES DA FAMÍLIA LÓGICA TTL

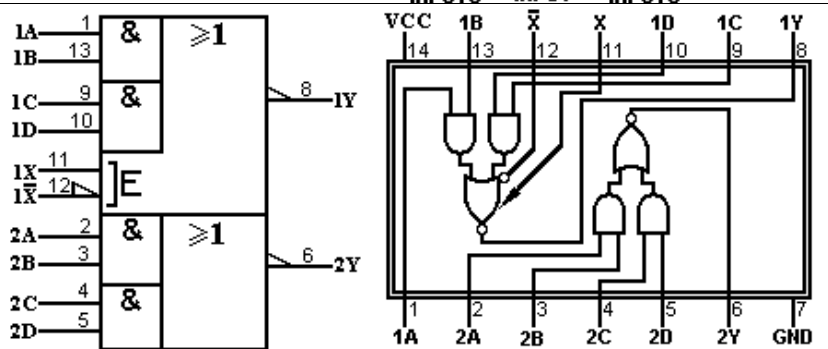
74LS48
BCD – To – Seven–Segment
Decoders/Drive



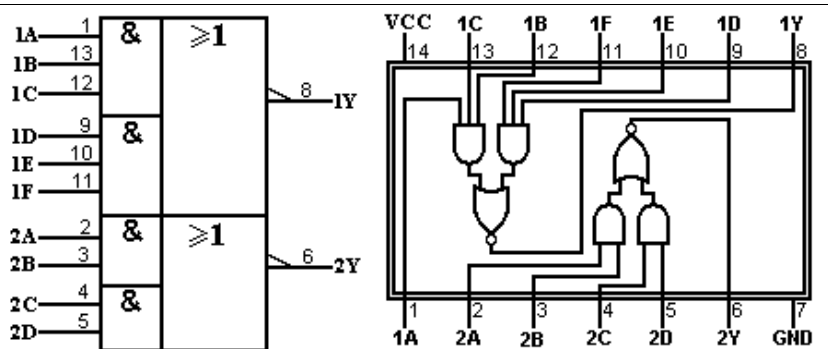
74LS49
BCD – To – Seven –Segment
Decoders/Drive



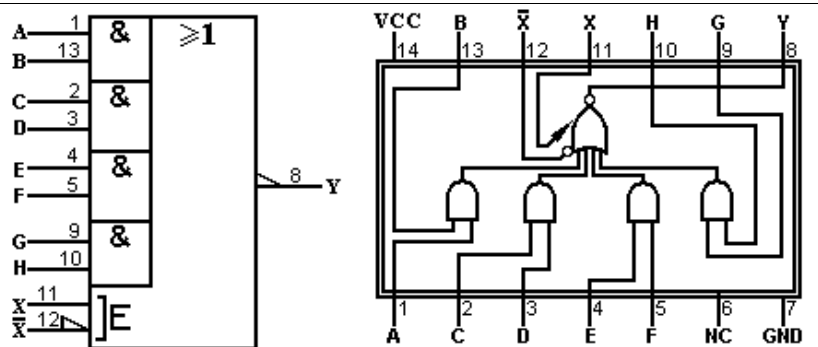
74LS50
Dual 2-Wide 2-Input
And-Or-Invert Gates
(One Gate Expandable)



74LS51
An-Or-Invert Gates

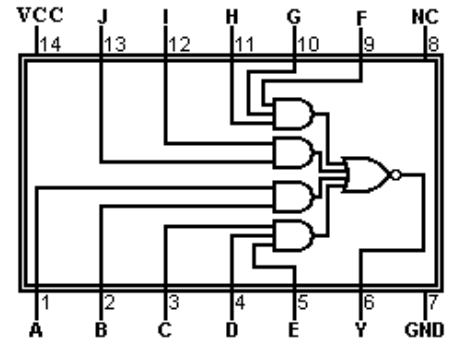
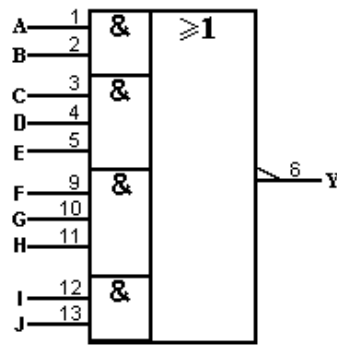


7453
Expandable 4-Wide
And-Or-Invert Gates

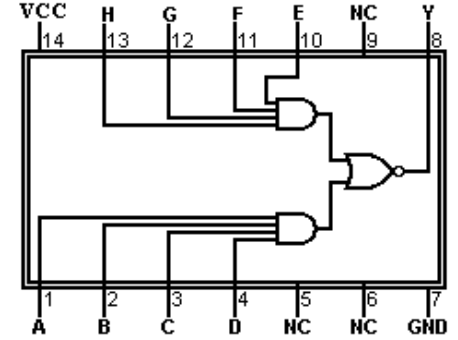
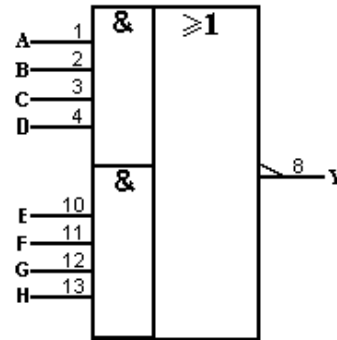


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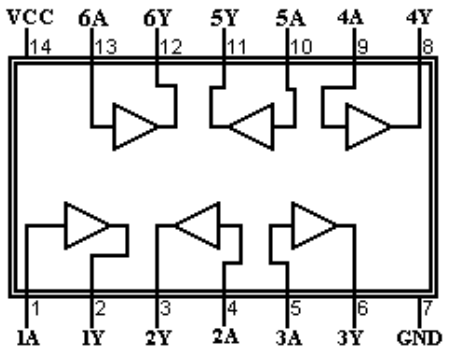
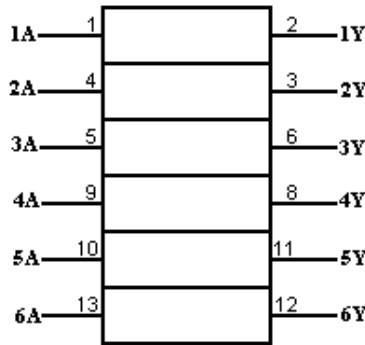
74LS54
4-Wide
And-Or-Invert Gates



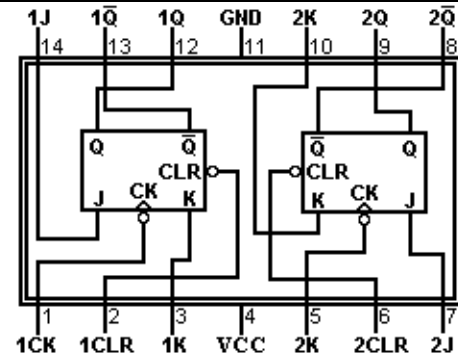
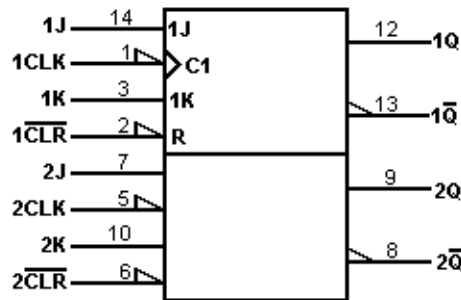
74LS55
2-Wide 4-Input
And-Or-Invert Gates



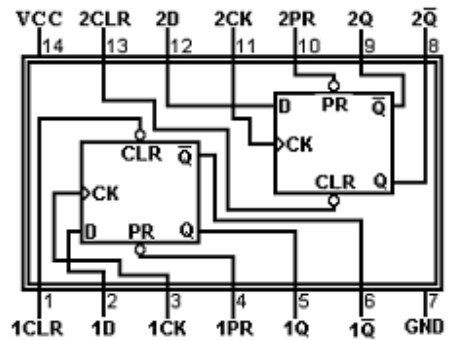
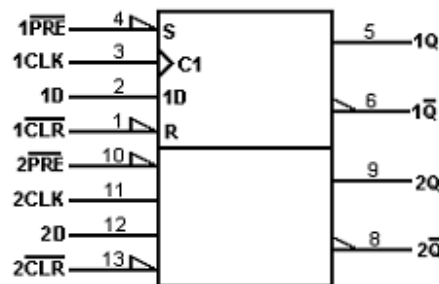
74LS63
Hex Current-Sensing
Interface Gates



74LS73
Dual J-K Flip-Flops
With Clear

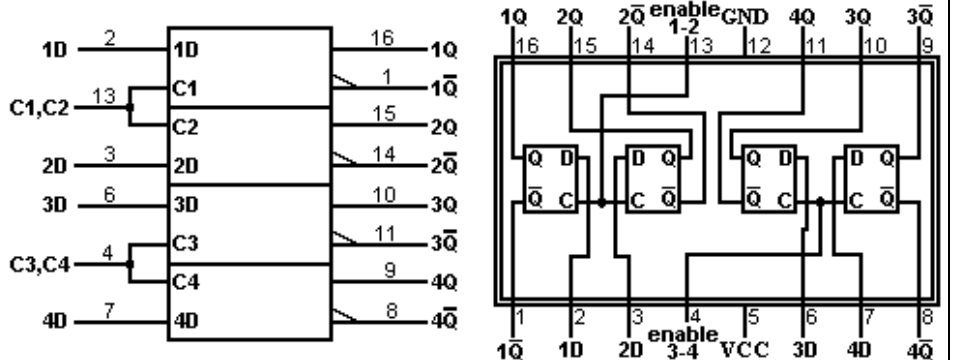


74LS74
Dual D-Type
Positive-Edge-Triggered
Flip-Flops With Preset
And Clear

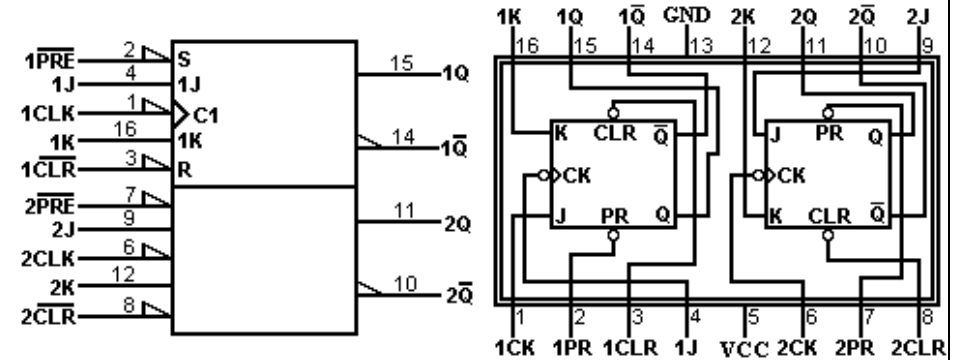


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

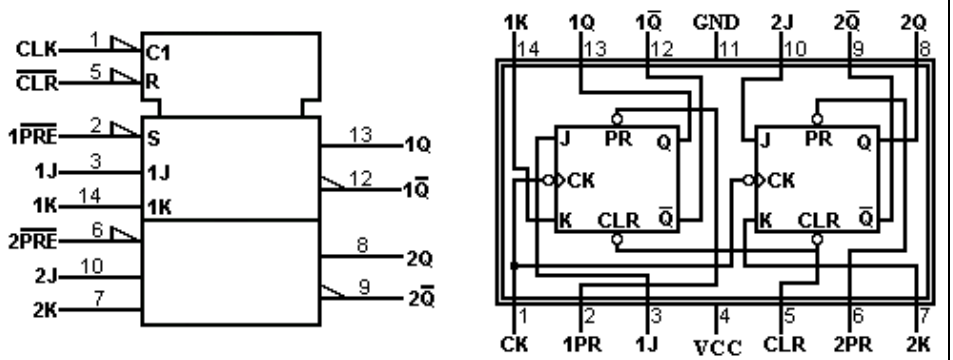
74LS75
4-Bit Bistable Latches



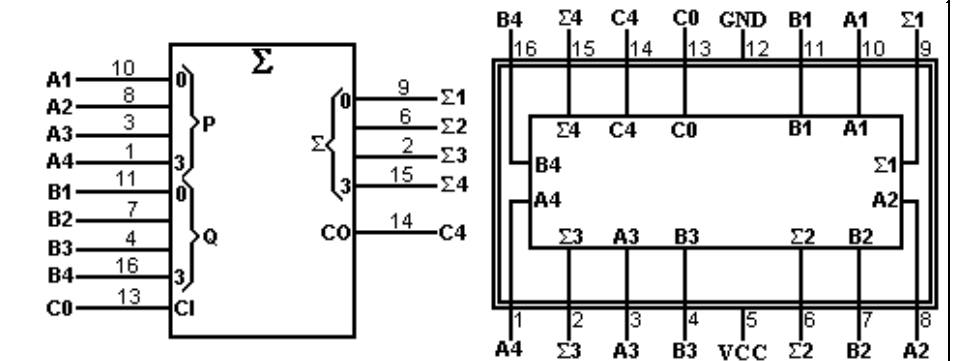
74LS76
Dual J-K Flip-Flops
With Preset and Clear



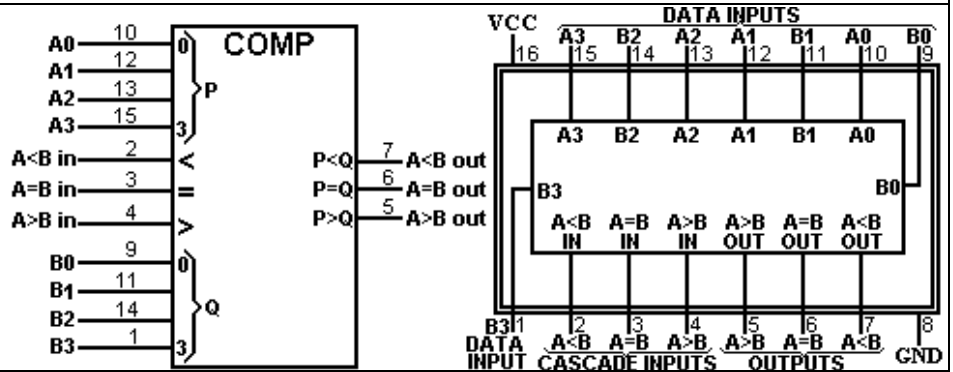
74LS78
Dual J-K Flip-Flops
With Preset,
Common Clear and
Common Clock



74LS83
4-Bit Binary Full Adders
With Fast Carry

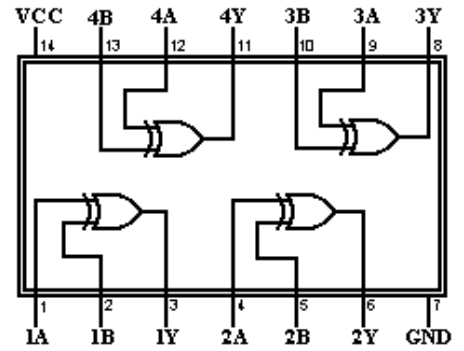
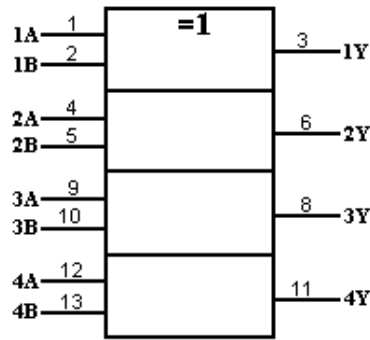


74LS85
4-Bit Magnitude
Comparator

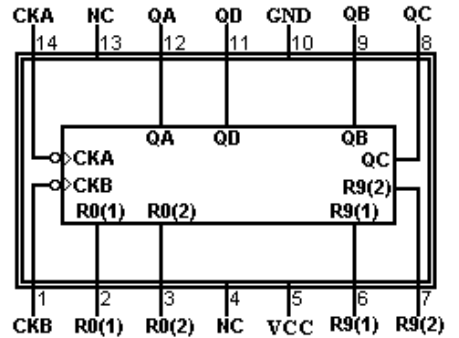
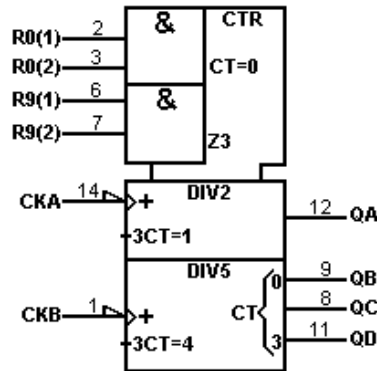


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

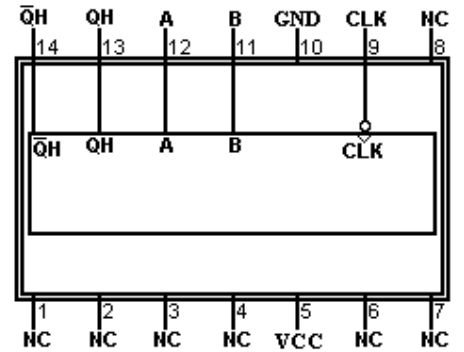
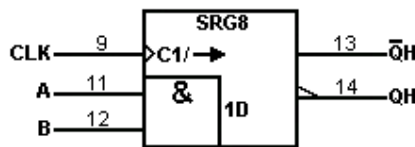
74LS86
Quadruple 2-Input
Exclusive-Or Gates



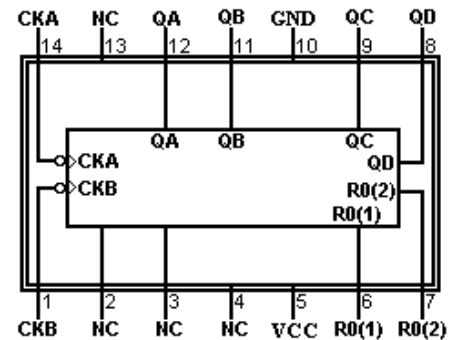
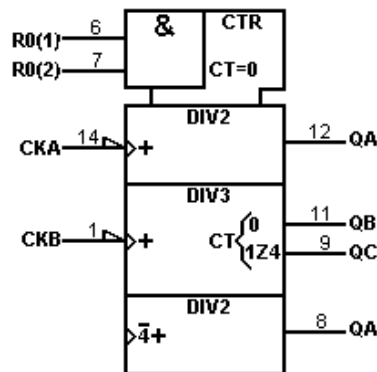
74LS90
Decade Counters



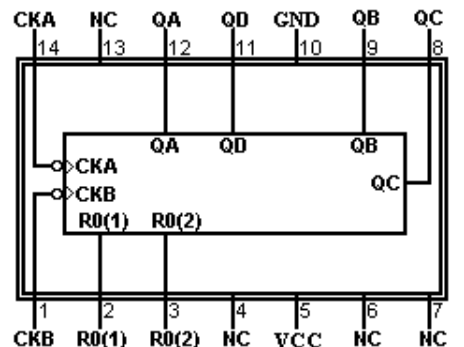
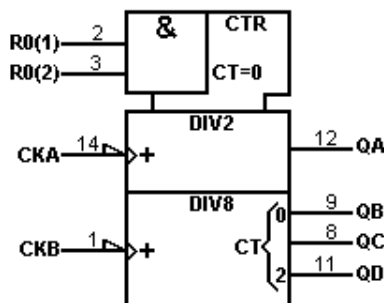
74LS91
8-Bit Shift Registers



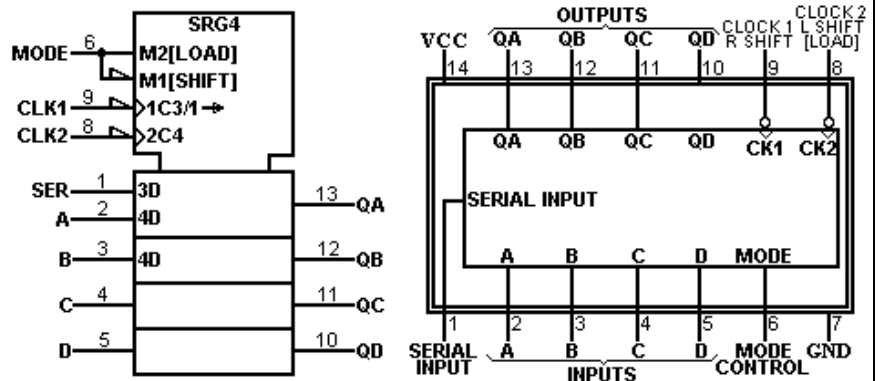
74LS92
Divide-By-Twelve Counters



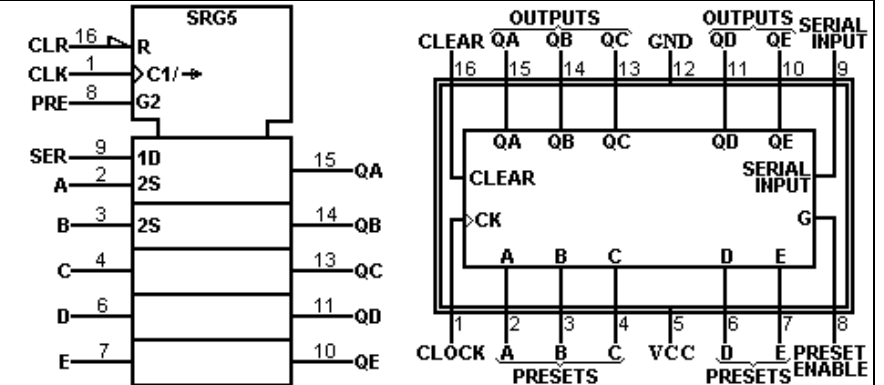
74LS93
4-Bit Binary Counters



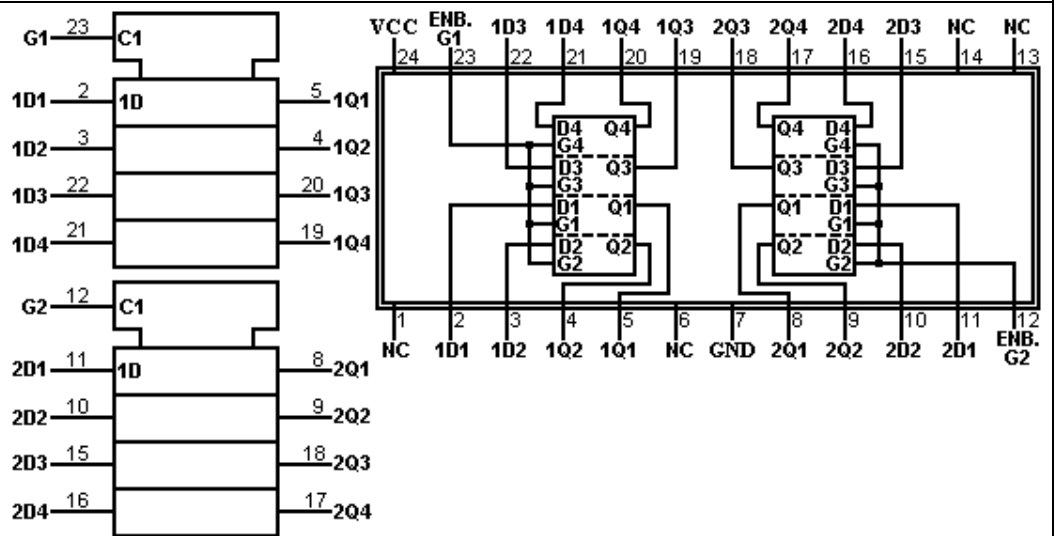
74LS95
4-Bit Shift Registers



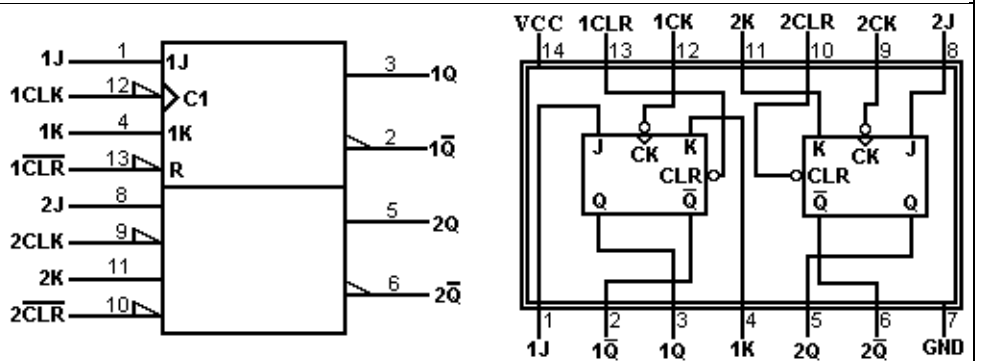
74LS96
5-Bit Shift Registers



74100
8-Bit Bistable Latches



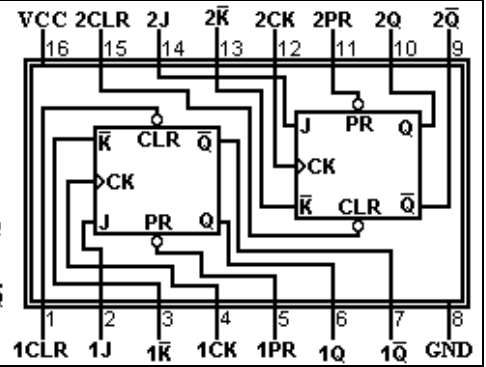
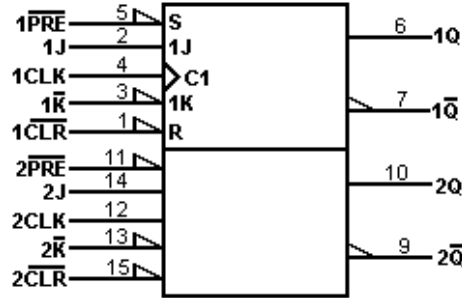
74LS107
Dual J-K Master-Slave Flip-Flops With Clear



“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

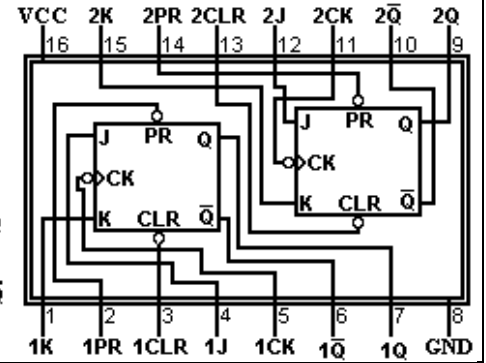
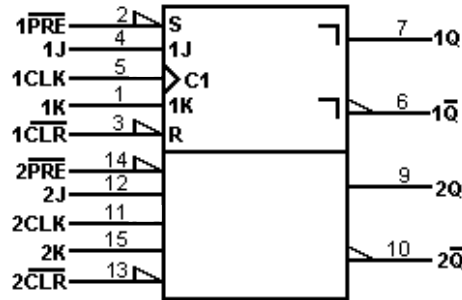
74LS109

**Dual J-K̄ Positive
Edge-Triggered
Flip-Flop With Preset
And Clear**



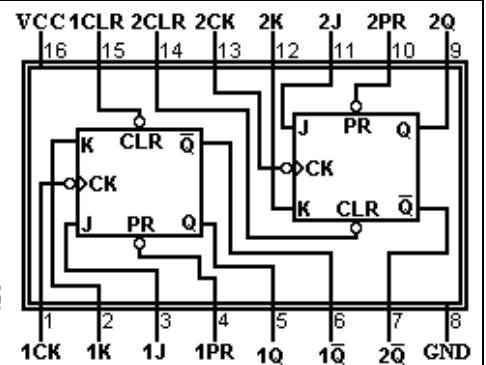
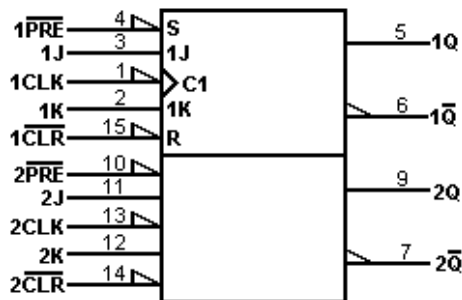
74111

**Dual J-K Master-Slave
Flip-Flops With
Data Lockout**



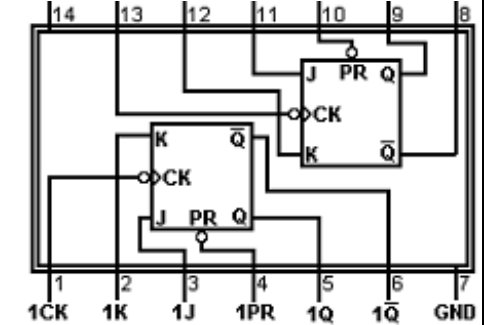
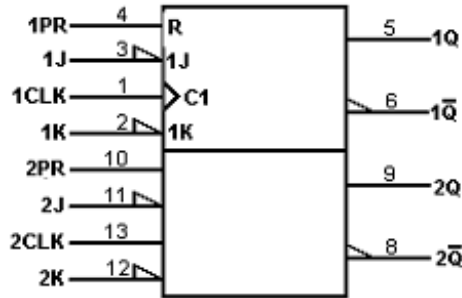
74LS112

**Dual J-K
Negative-Edge-Triggered
Flip-Flop With Preset
And Clear**



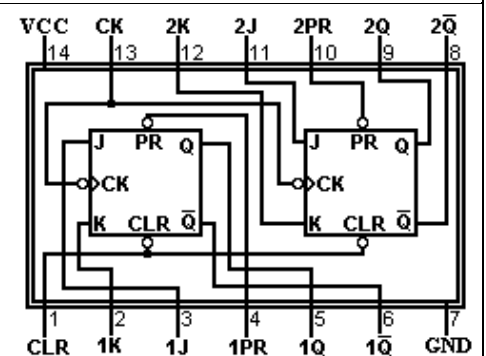
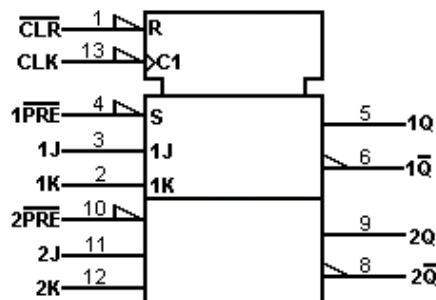
74LS113

**Dual J-K
Negative-Edge-Triggered
Flip-Flop With Preset**



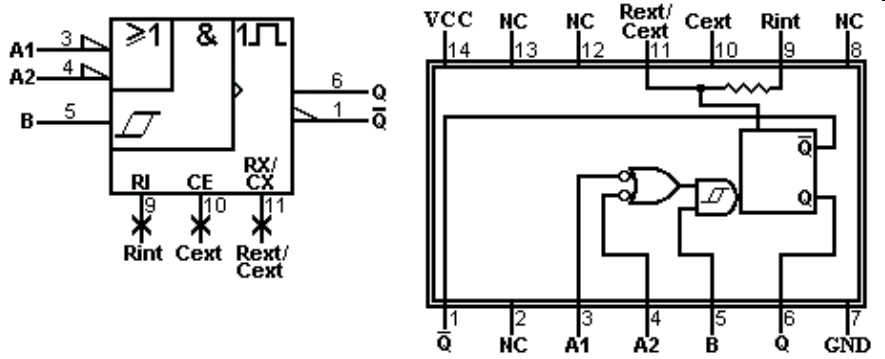
74LS114

**Dual J-K
Negative-Edge-Triggered
Flip-Flop With Preset
Common Clear and
Common Clock**

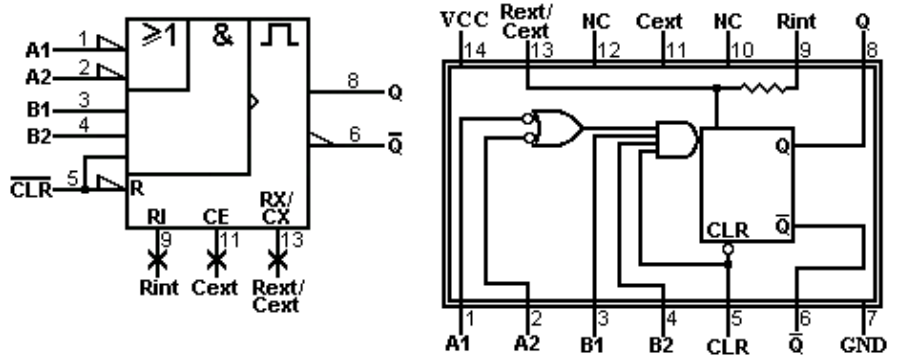


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

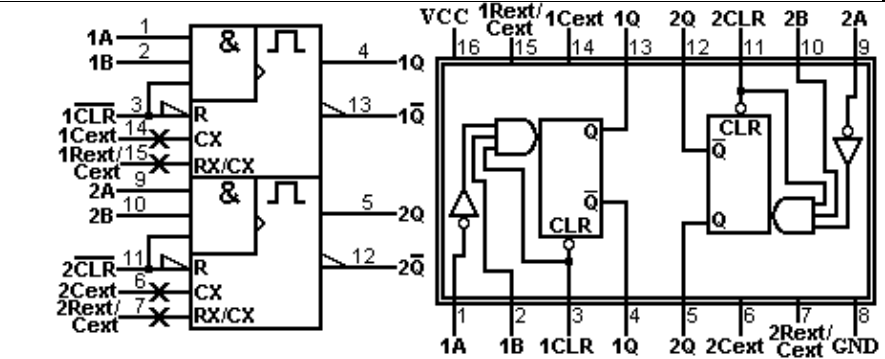
74LS121
Monostabe Multivibrators



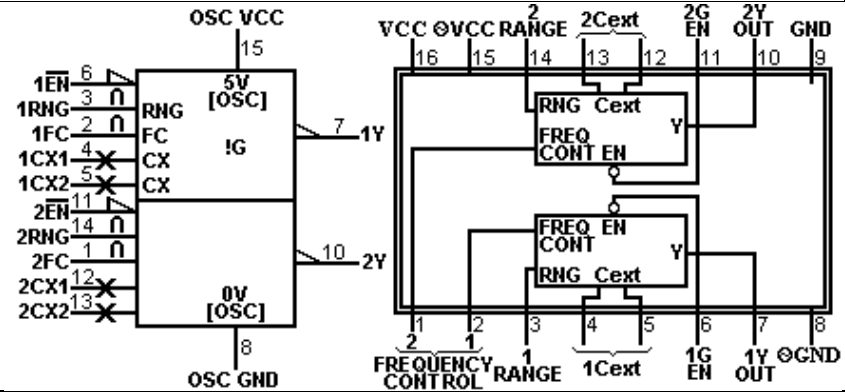
74LS122
Retriggerable Monostabe Multivibrators With Clear



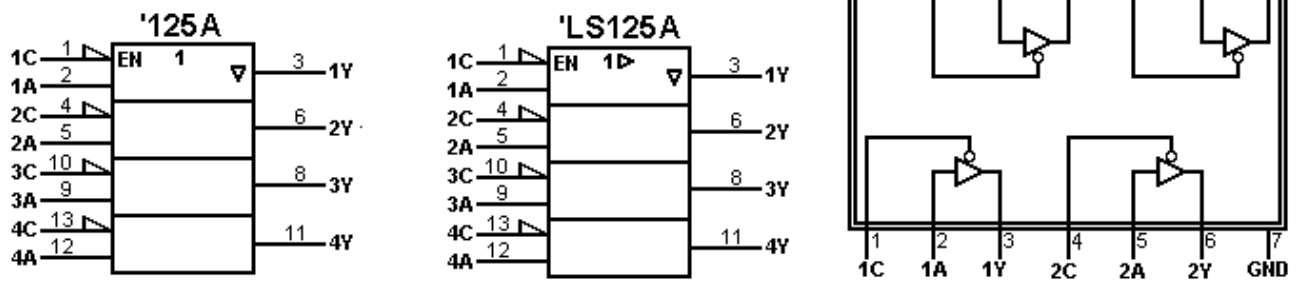
74LS123
Dual Retriggerable Monostable Multivibrators With Clear



74LS124
Dual Voltage-Controlled Oscillators

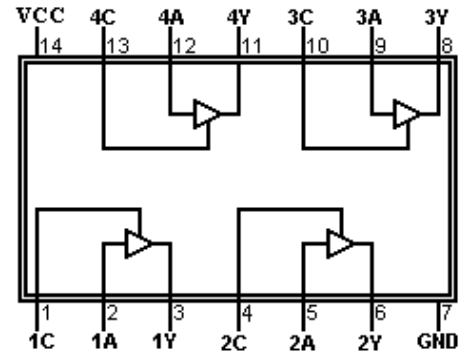
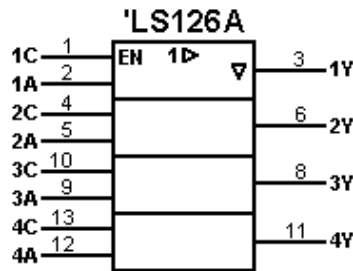
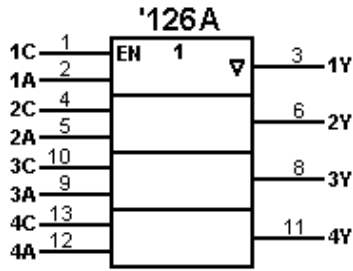


74LS125
Quadruple Bus Buffer Gates With Three-State Outputs



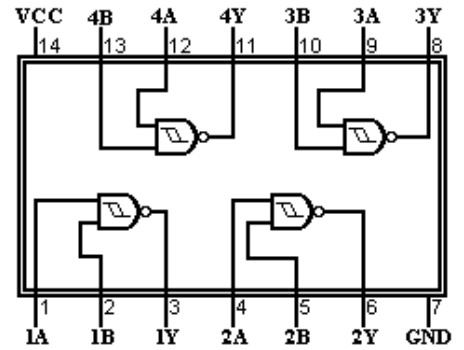
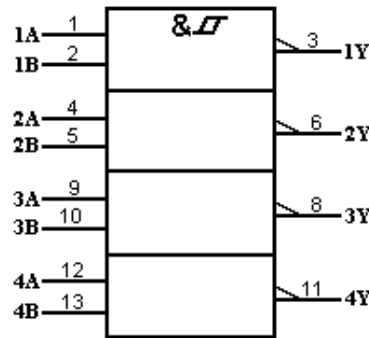
74LS126

Quadruple Bus Buffer Gates With Three-State Outputs



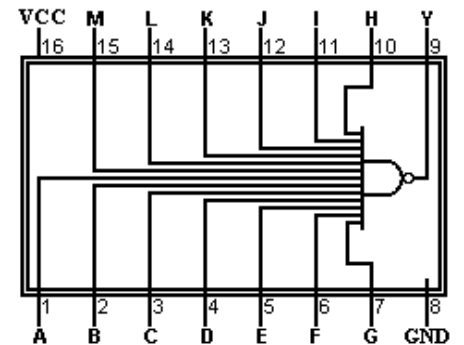
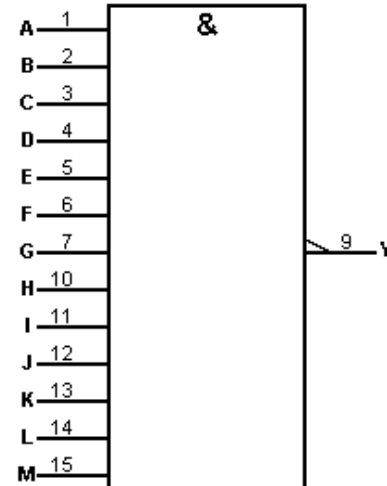
74LS132

Quadruple 2-Input Positive-Nand Schmitt Triggers



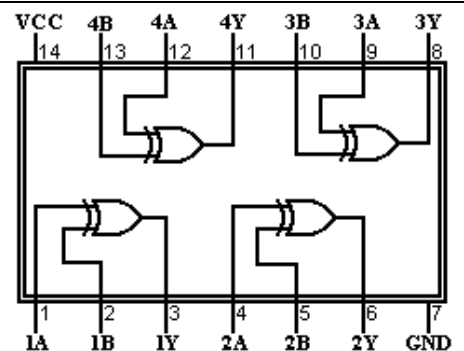
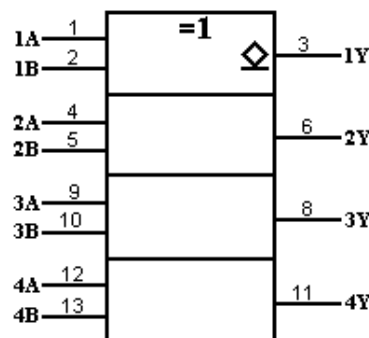
74LS133

13-Input Positive-Nand Gates



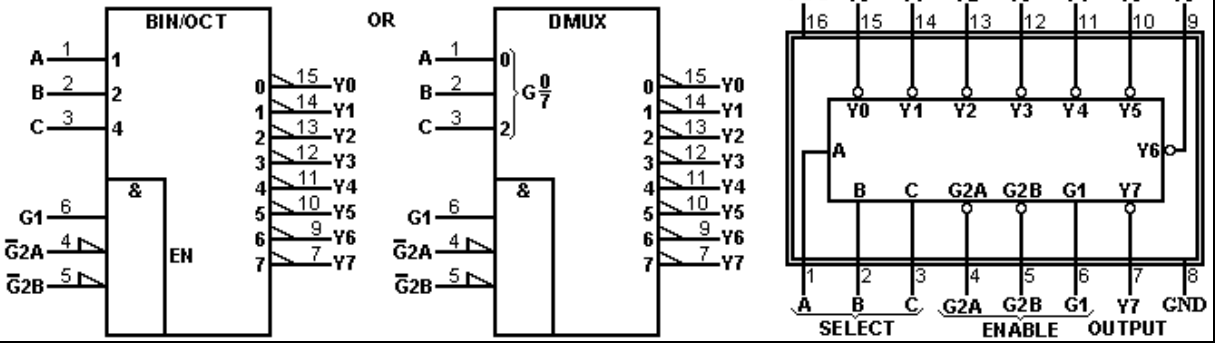
74LS136

Quad Exclusive-Or Gates With Open-Collectors Outputs



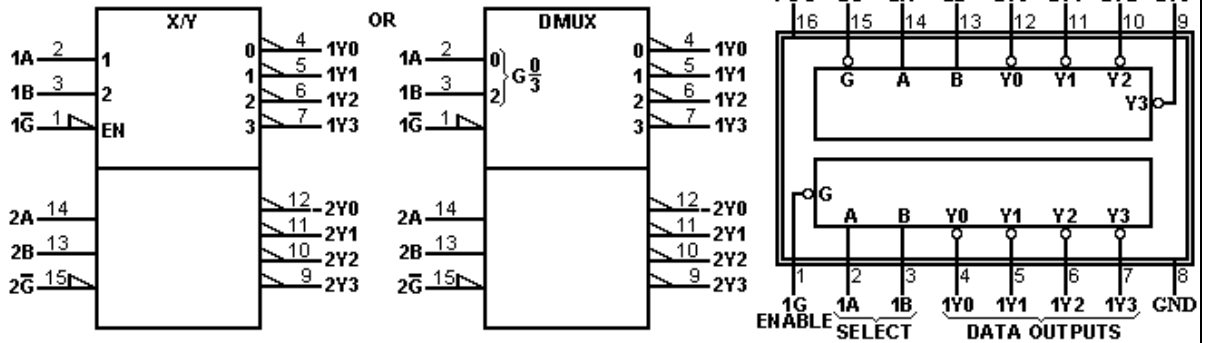
74LS138

3-To-8 Line Decoders/Demultiplexers



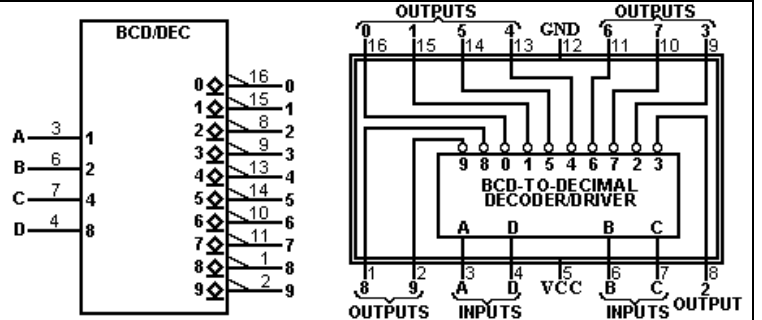
74LS139

Dual 2-To-4 Line Decoders/Demultiplexers



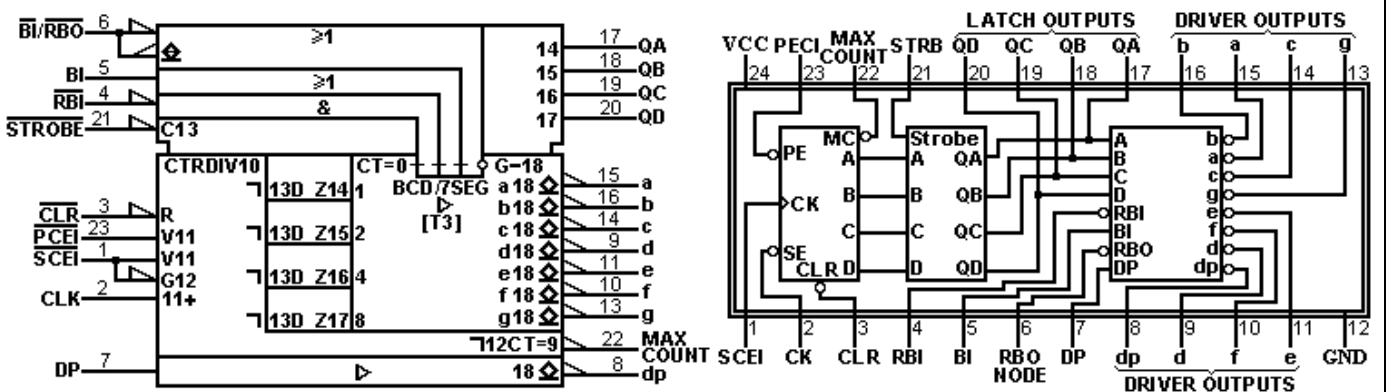
74LS141

BCD-To-Decimal Decoder/Driver



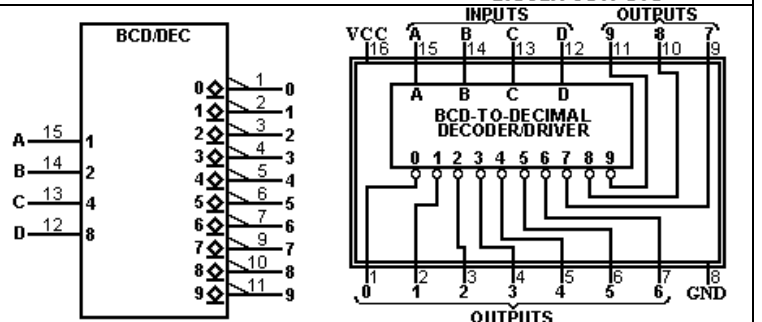
74LS143

Counter/Latch/Decoder/Driver



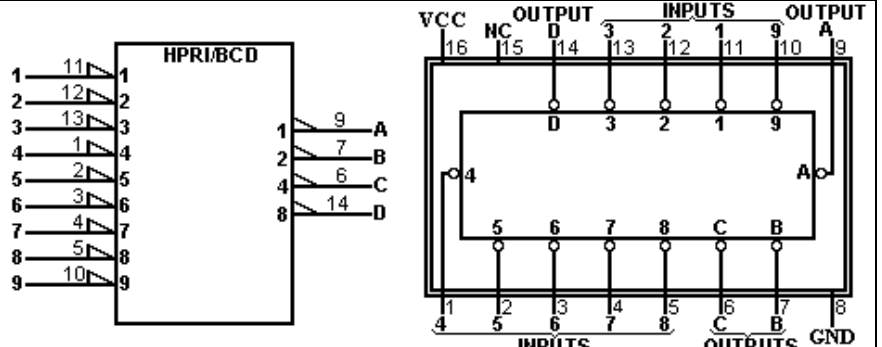
74LS145

BCD-To-Decimal Decoder/Driver

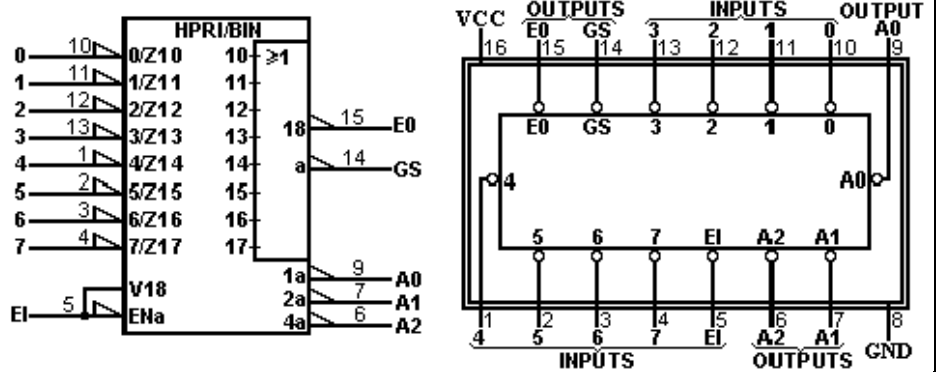


"DATA SHEETS" DE COMPONENTES DA FAMÍLIA LÓGICA TTL

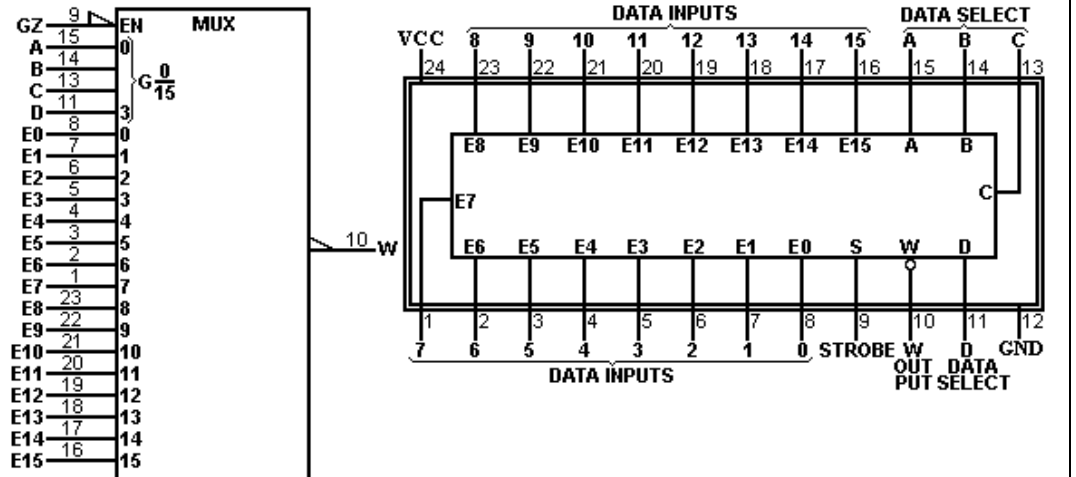
74LS147
10-Line Decimal to 4-Line
BCD Priority Encoders



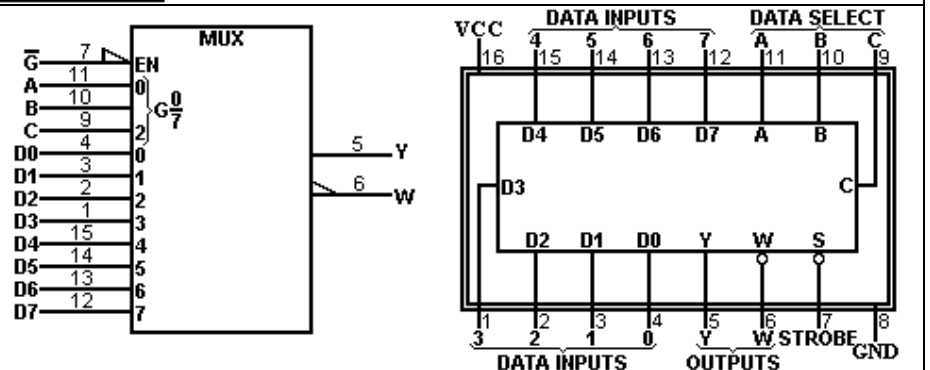
74LS148
8-Line to 3-Line
Octal Priority Encoders



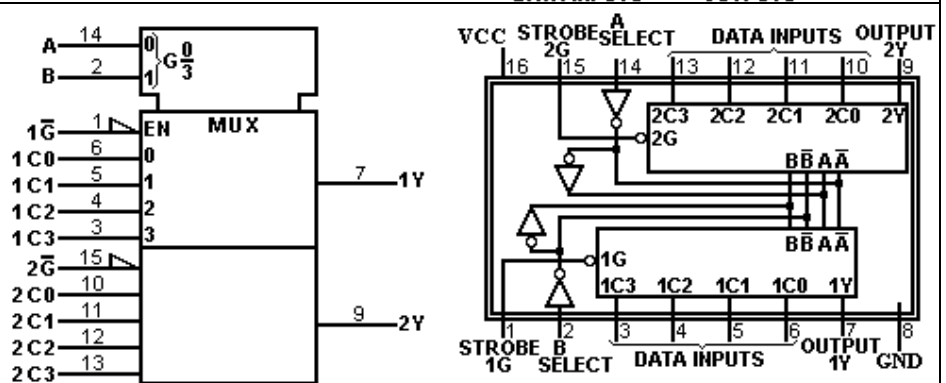
74LS150
1-of-16 Data
Selector/Multiplexers



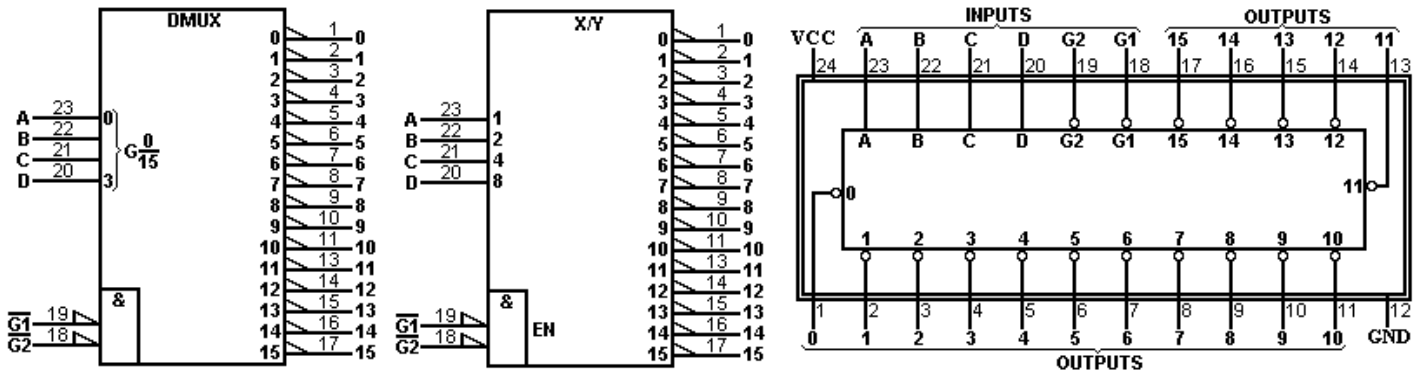
74LS151
1-of-8 Data
Selector/Multiplexers



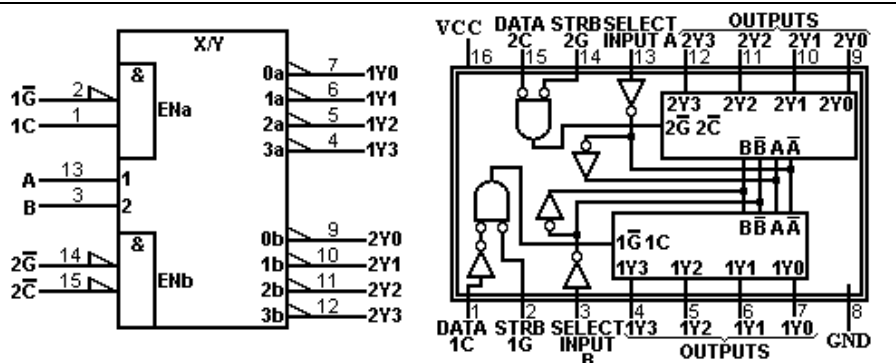
74LS153
Dual 4-Line to
1-Line Data
Selector/Multiplexers



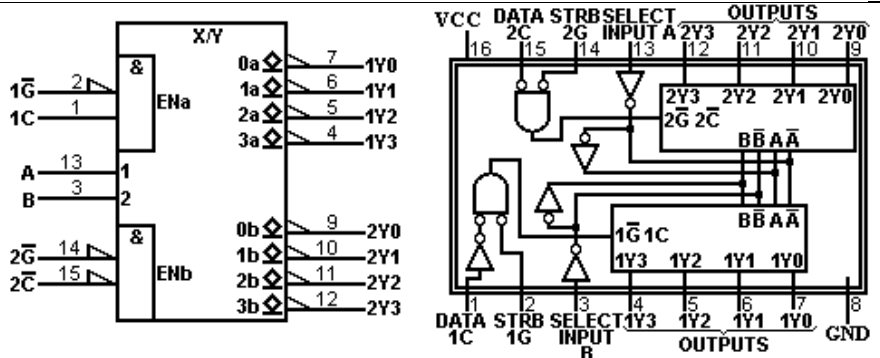
74154
4-Line to 16-Line Decoders/Demultiplexers



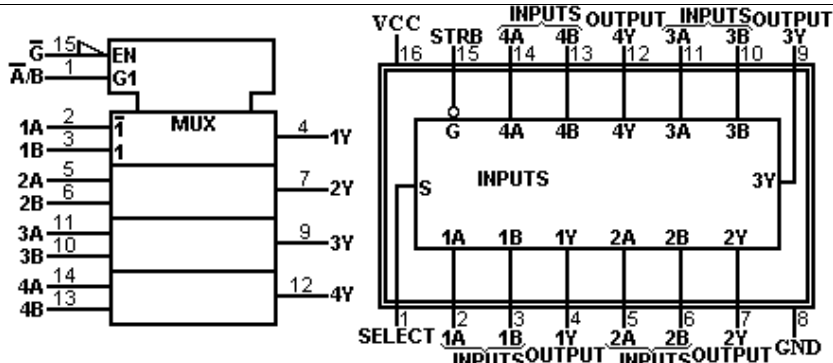
74LS155
Decoders/Demultiplexers
(Totem-Pole Outputs)



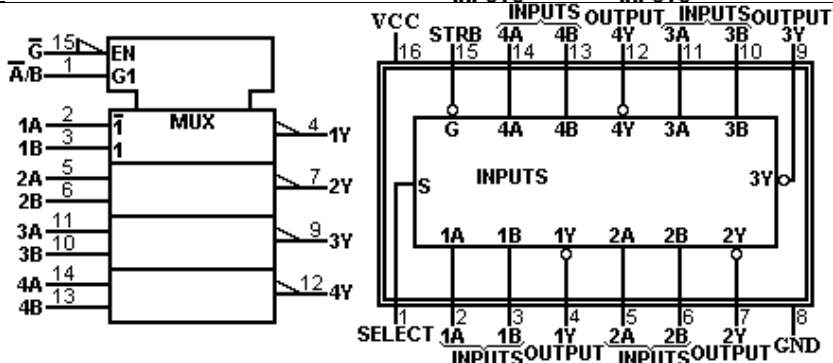
74LS156
Decoders/Demultiplexers
(Open-Collector Outputs)



74LS157
Quad 2-To 1-Line Data
Selector/Multiplexers
(Noninverted Data Outputs)

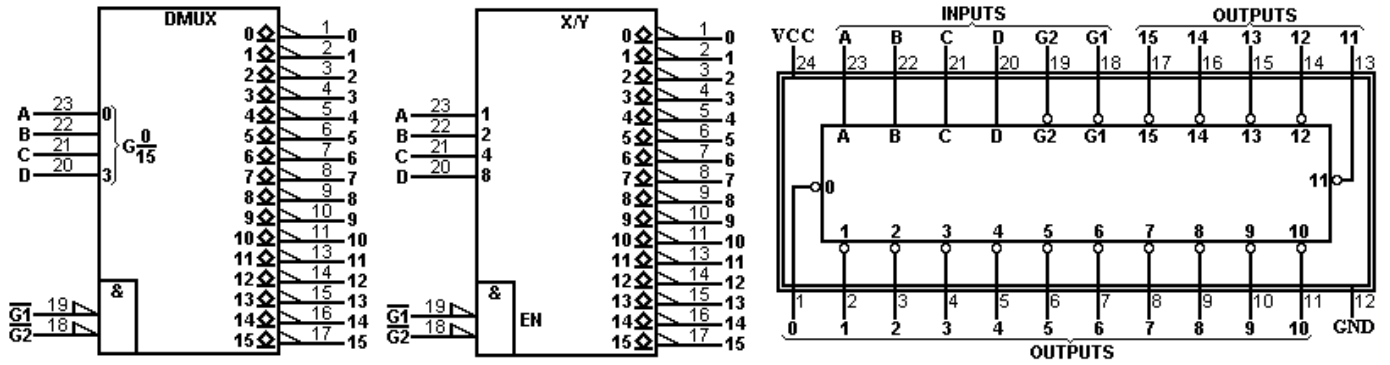


74LS158
Quad 2-To 1-Line Data
Selector/Multiplexers
(Inverted Data Outputs)



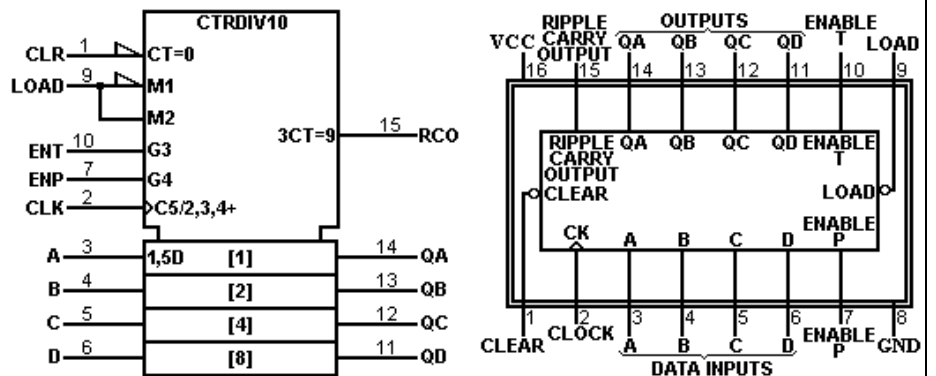
74159

4-To 16-Line Decoders/Demultiplexers (Open-Collector Outputs)



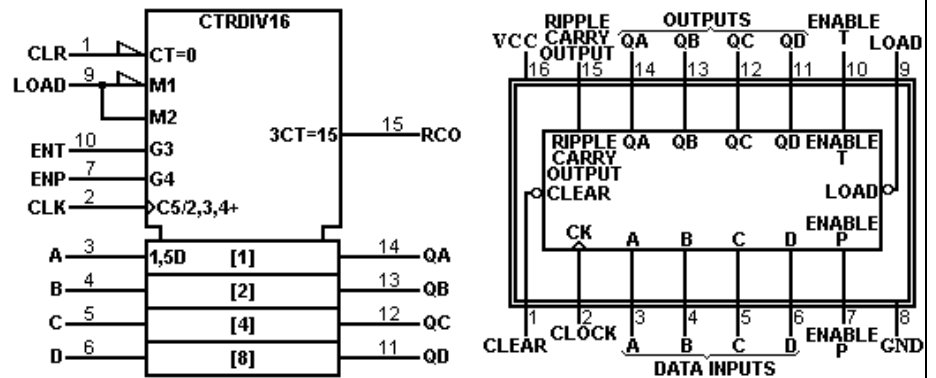
74LS160

Synchronous 4-Bit Counters (Decade, Direct Clear)



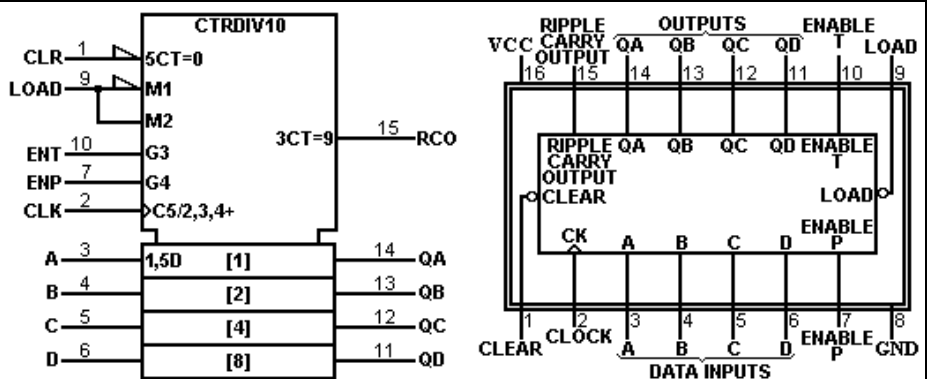
74LS161

Synchronous 4-Bit Counters (Binary, Direct Clear)



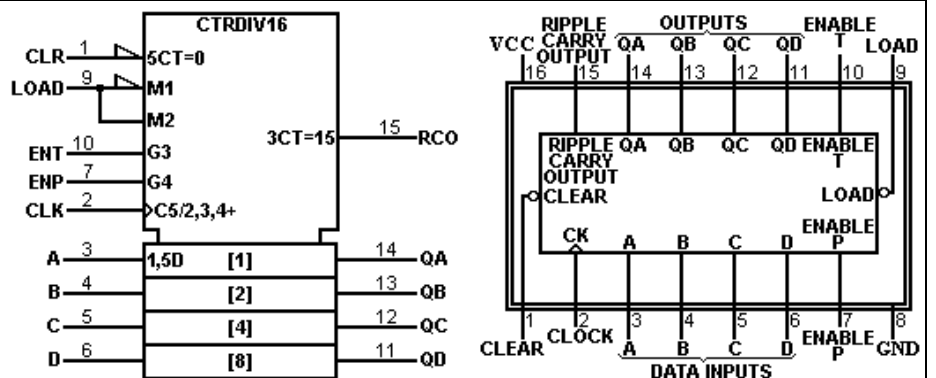
74LS162

Synchronous 4-Bit Counters (Decade, Synchronous Clear)



74LS163

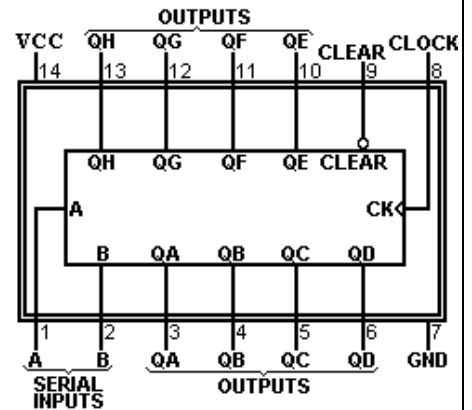
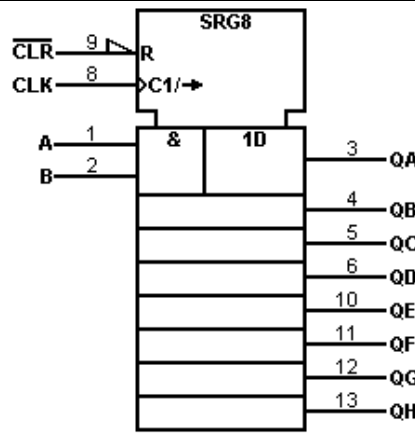
Synchronous 4-Bit Counters (Binary, Synchronous Clear)



“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

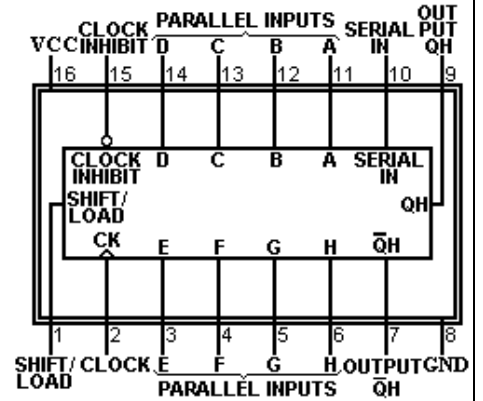
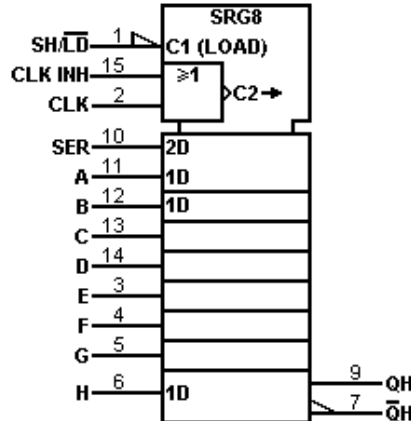
74LS164

8-Bit Parallel Output Serial Shift Registers



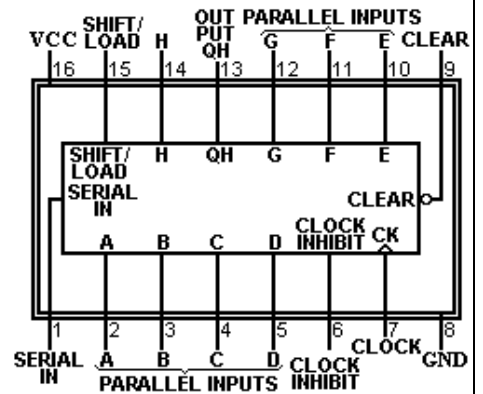
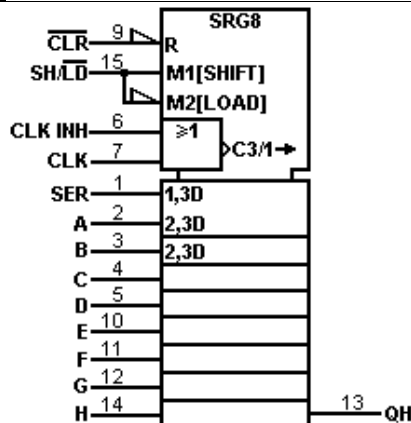
74LS165

Parallel-Load 8-Bit Shift Registers With Complementary Outputs



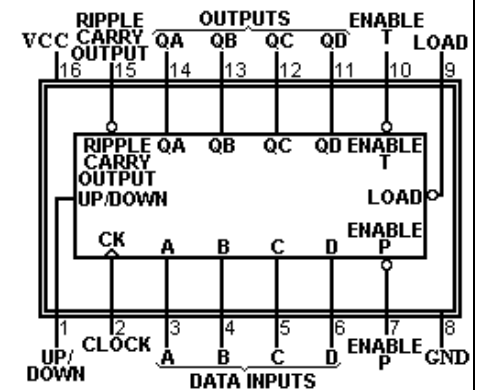
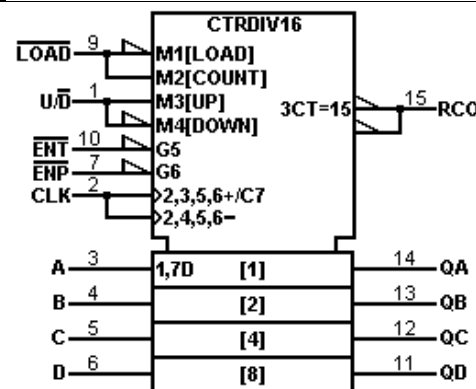
74LS166

8-Bit Shift Registers



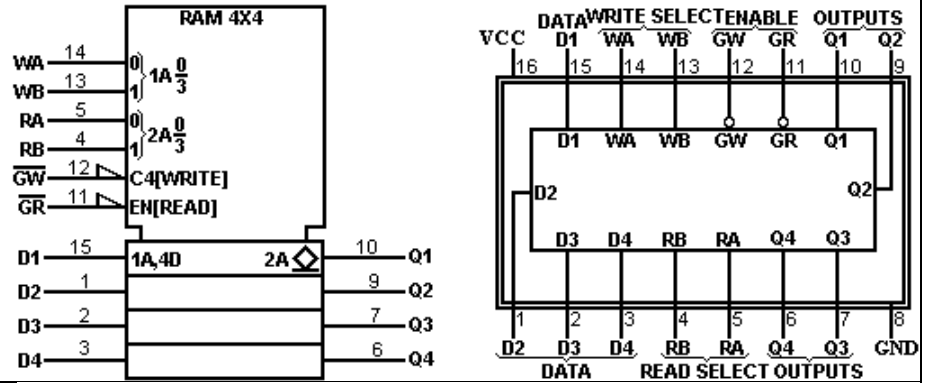
74LS169

4-Bit Up/Down Synchronous Clear (Binary)

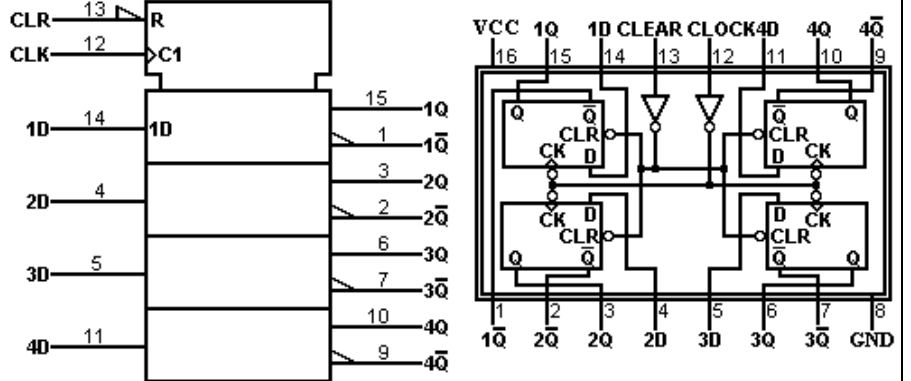


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

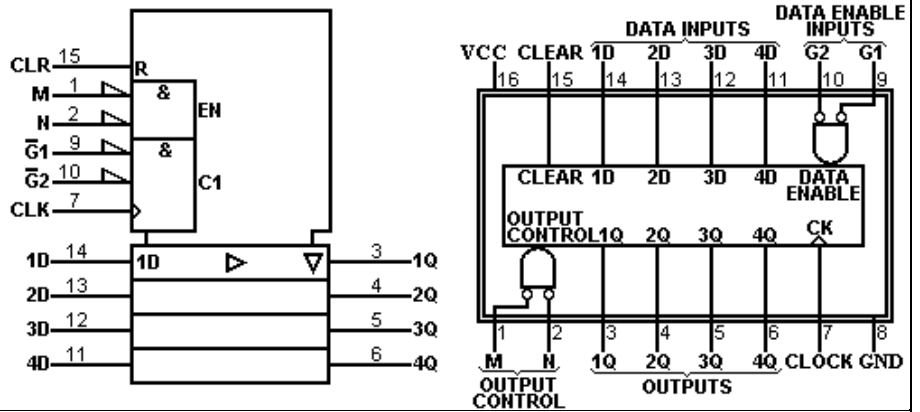
74LS170
4-By-4 Register Files
With Output Open Collector



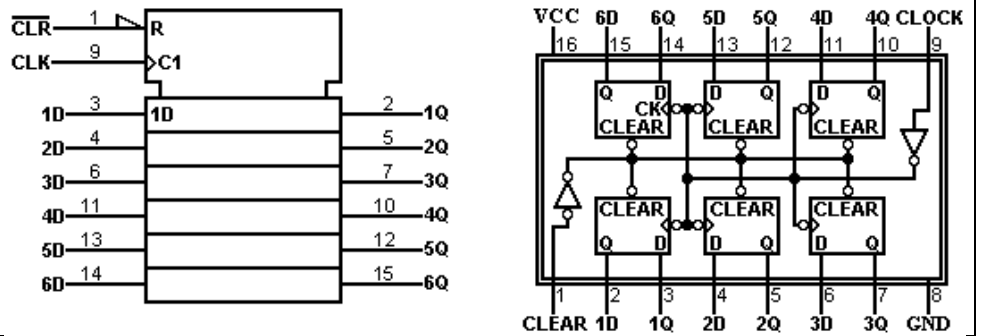
74LS171
Quadruple D-Type
Flip-Flops
With Clear



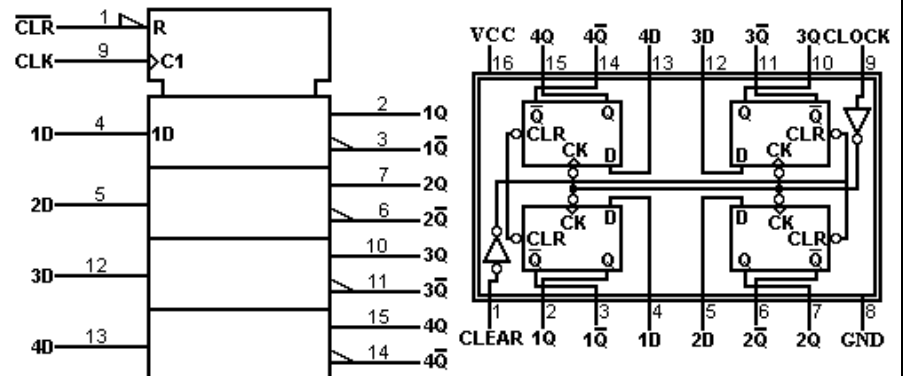
74LS173
4-Bit D-Type
Registers



74LS174
Hex D-Type
Flip-Flops

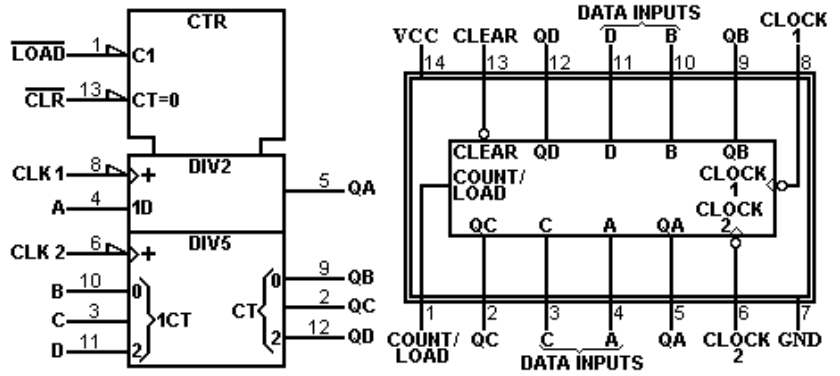


74LS175
Quad D-Type
Flip-Flops

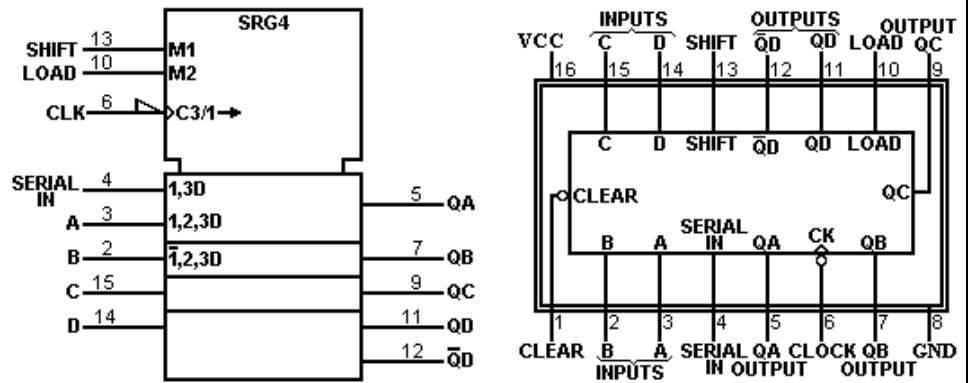


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

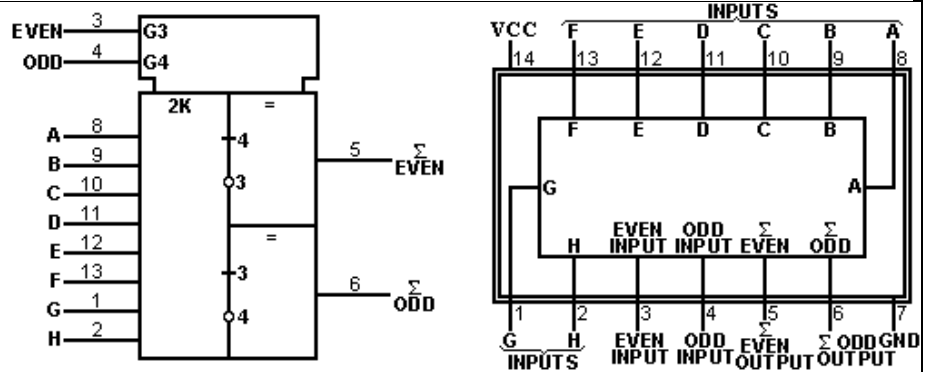
74176
Presetable
Counters/Latches
 (Decade (Bi-Quinary))



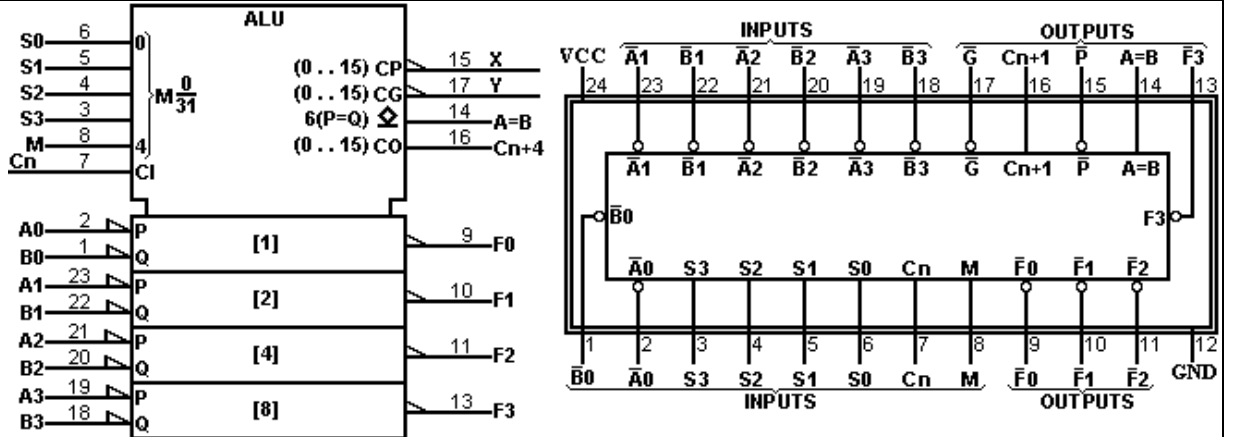
74179
4 – Bit Universal
Shift Registers



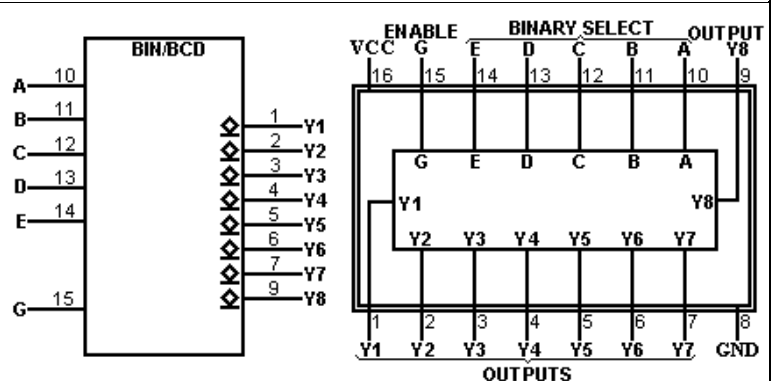
74180
9-Bit Odd/Even Paraty
Generators/Checkers



74LS181
Arithmetic
Logic
Units
/Function
Generators



74185
Code Converters
 (Binary-To-BCD)

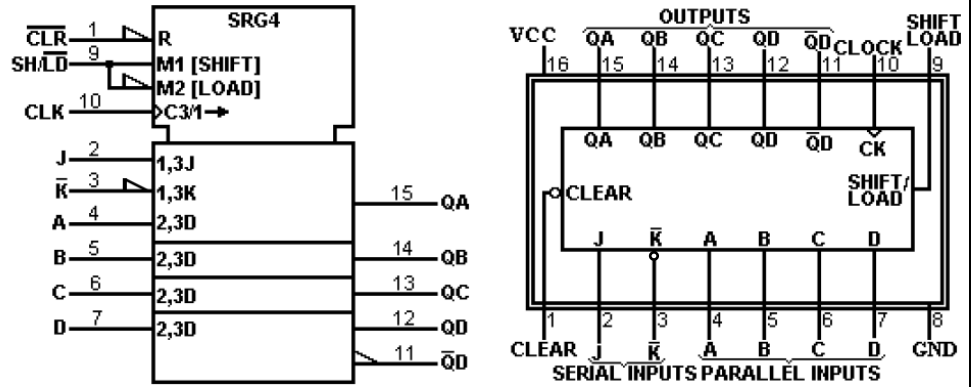


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

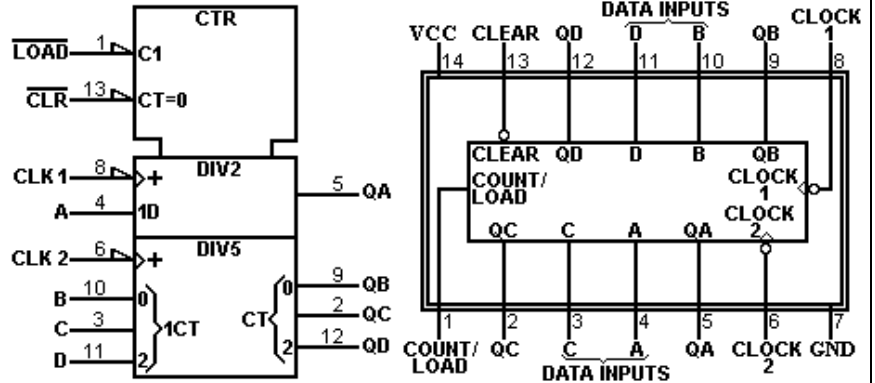
<p>74LS190 Synchronous Up/Down Counters (BCD)</p>		
<p>74LS191 Synchronous Up/Down Counters (Binary)</p>		
<p>74LS192 Synchronous Up/Down Dual Clock Counters (BCD With Clear)</p>		
<p>74LS193 Synchronous Up/Down Dual Clock Counters (Binary With Clear)</p>		
<p>74LS194 4-Bit Bidirectional Universal Shift Registers</p>		

“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

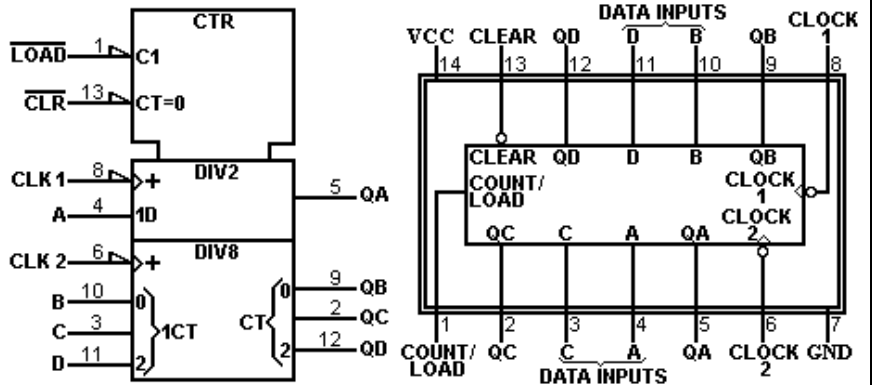
74LS195
4-Bit Parallel-Access
Shift Registers



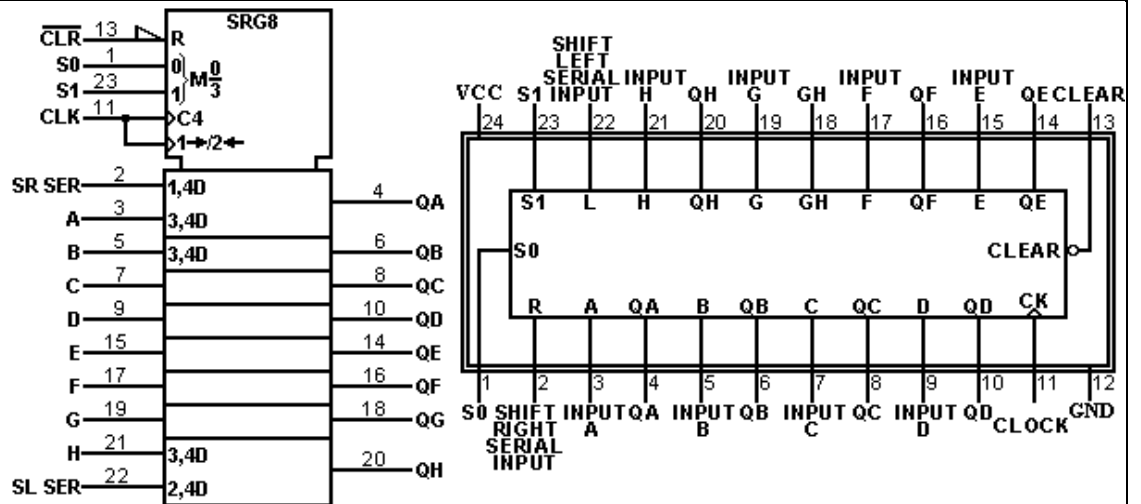
74LS196
Presetable Counters/Latches
(Decade/Bi-Quinary)



74LS197
Presetable Counters/Latches
(Binary)

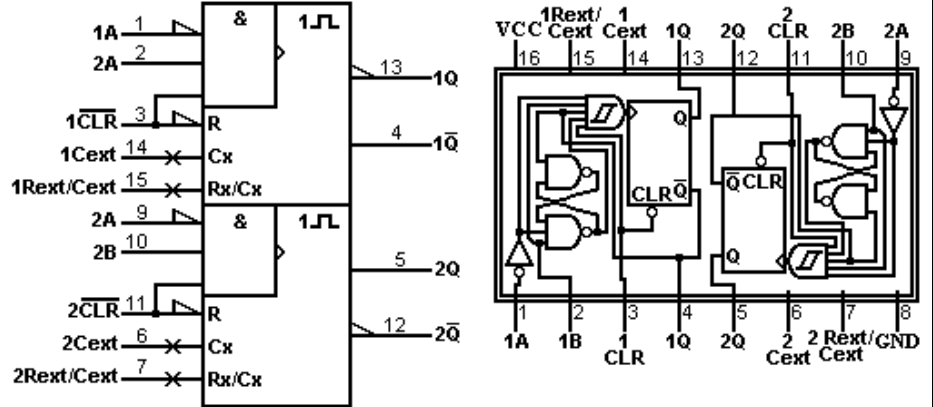


74198
8-Bit Bidirectional
Universal Shift
Registers

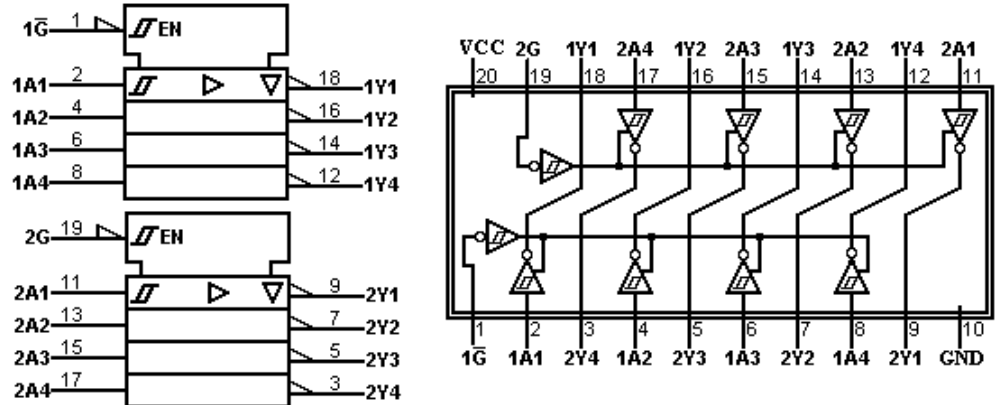


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

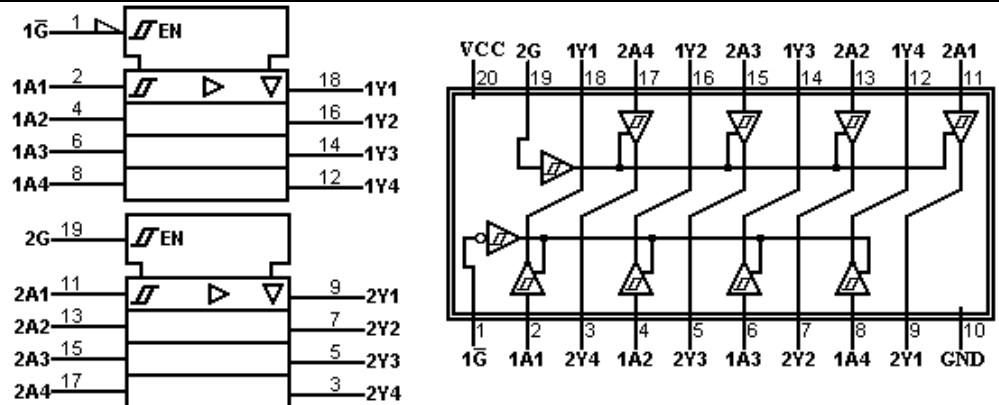
74LS221
Dual Monostable
Multivibrators



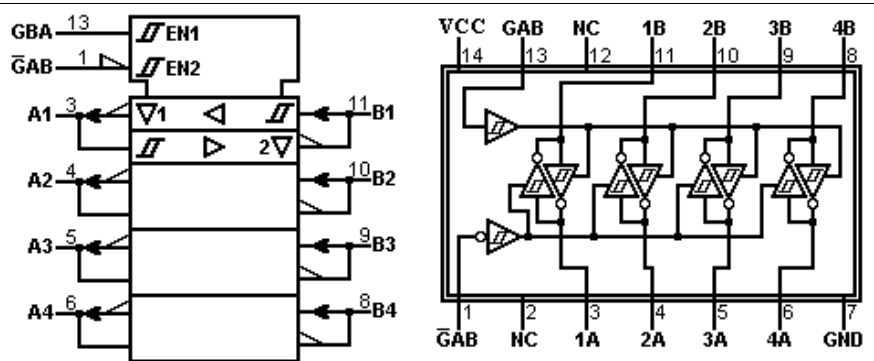
74LS240
Octal Buffers/Line
Drivers/Line
Receivers



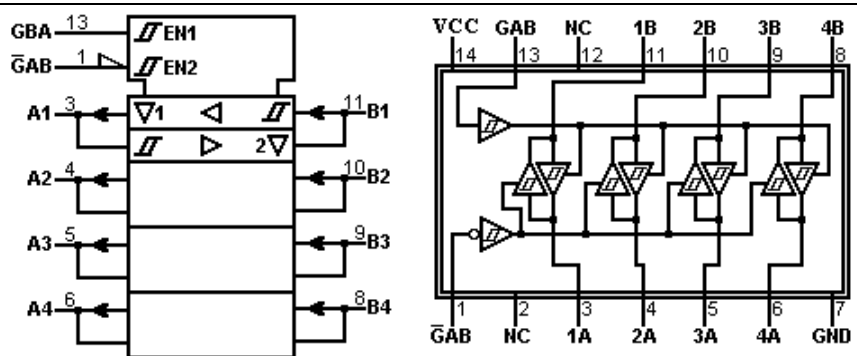
74LS241
Octal Buffers/Line
Drivers/Line
Receivers



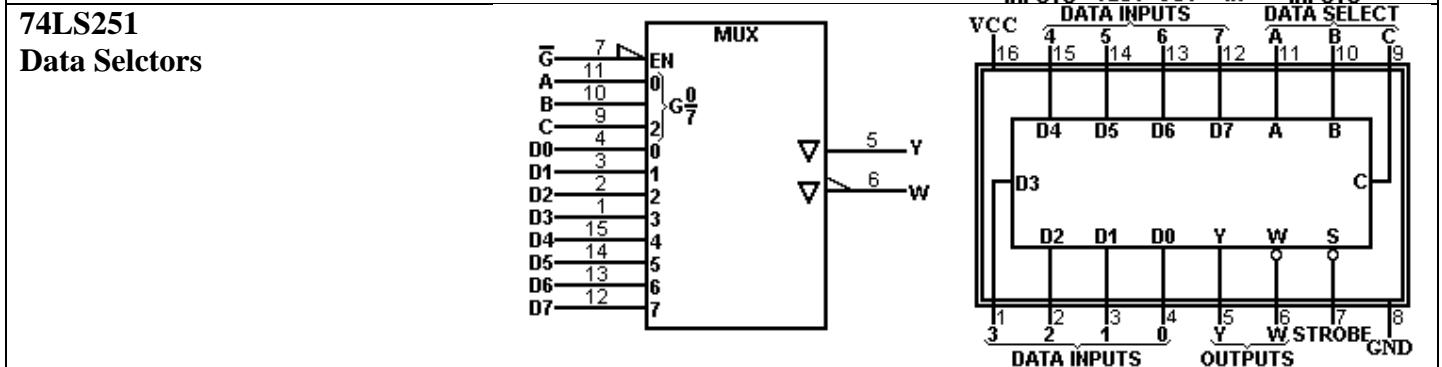
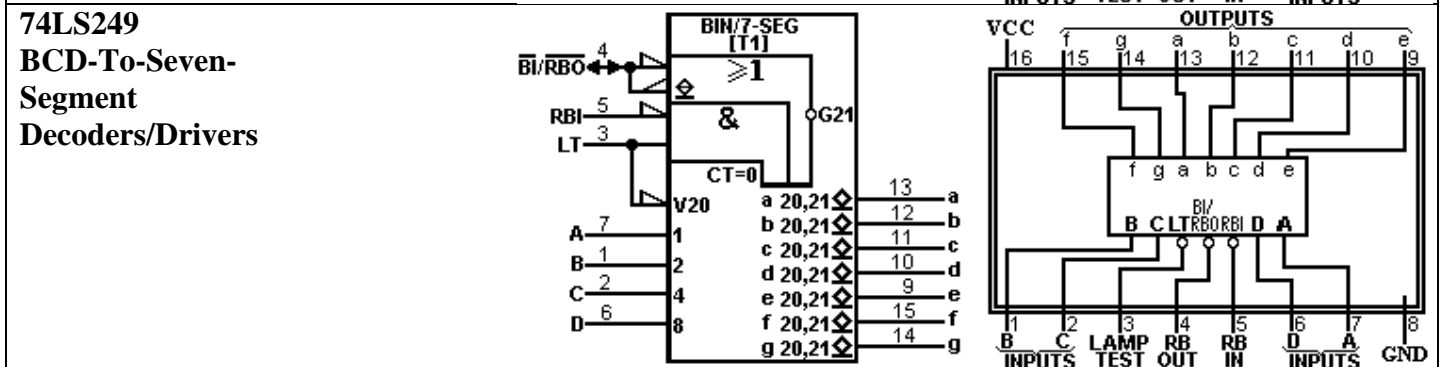
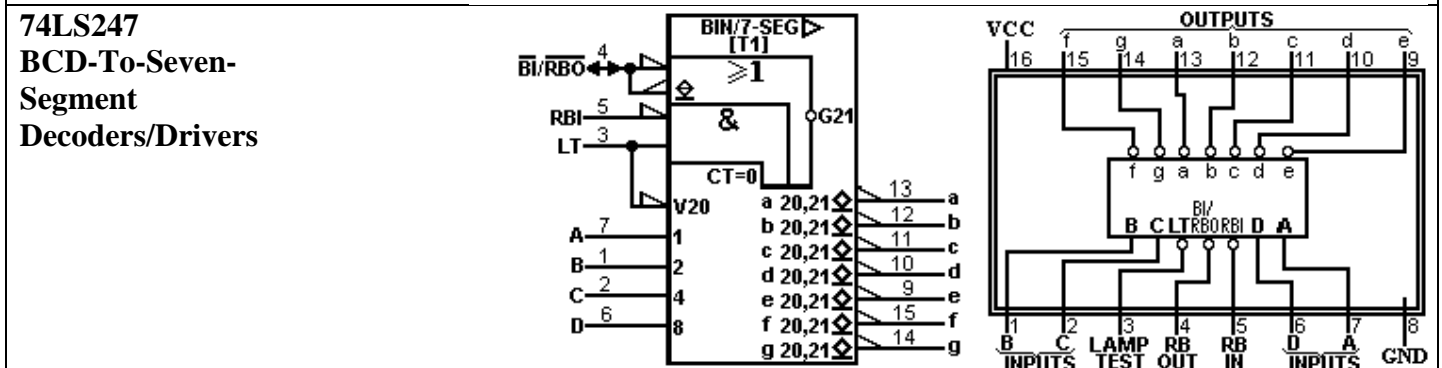
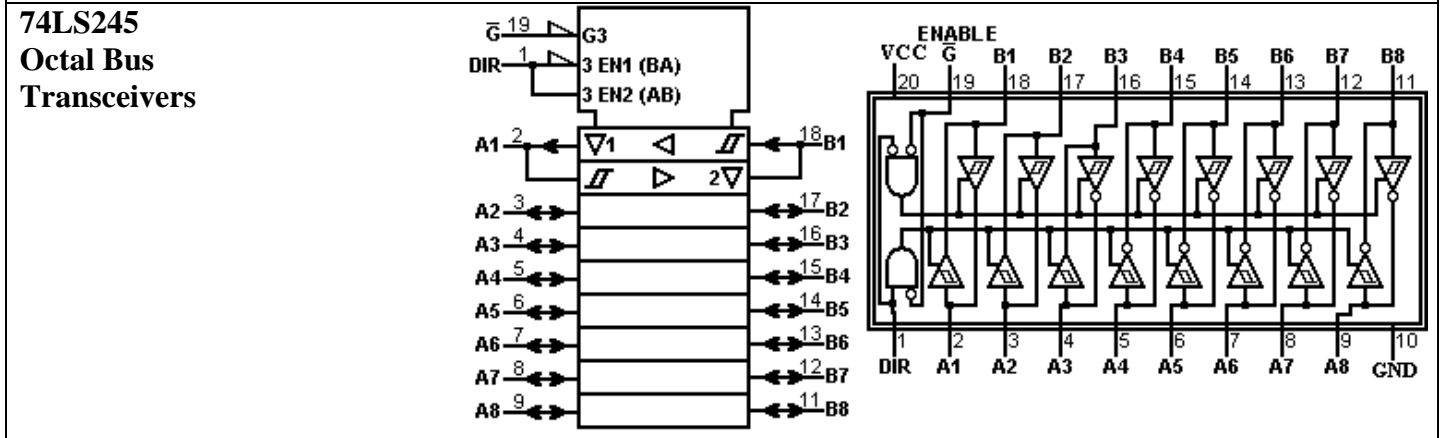
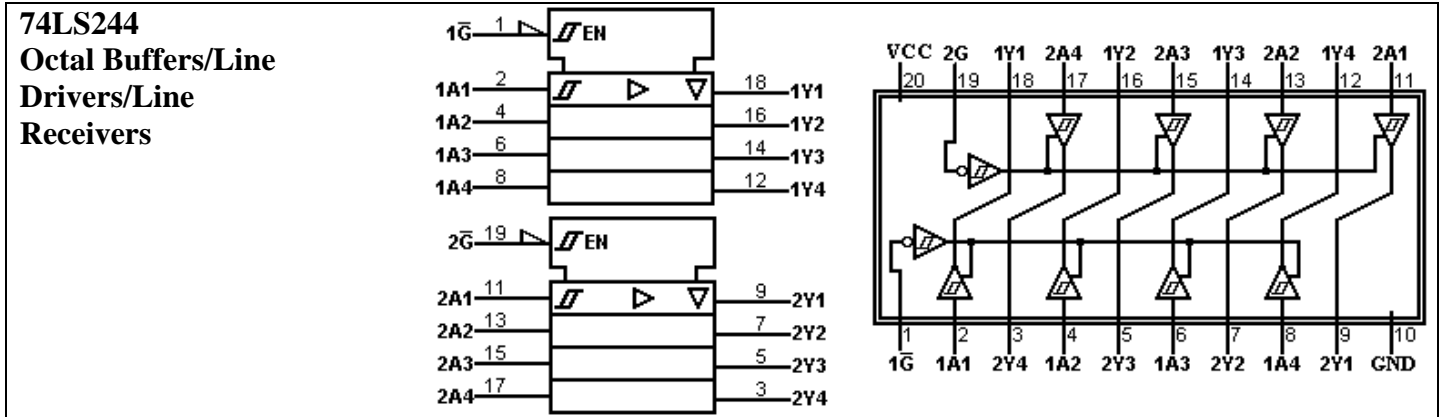
74LS242
Quadruple Bus
Transceivers



74LS243
Quadruple Bus
Transceivers

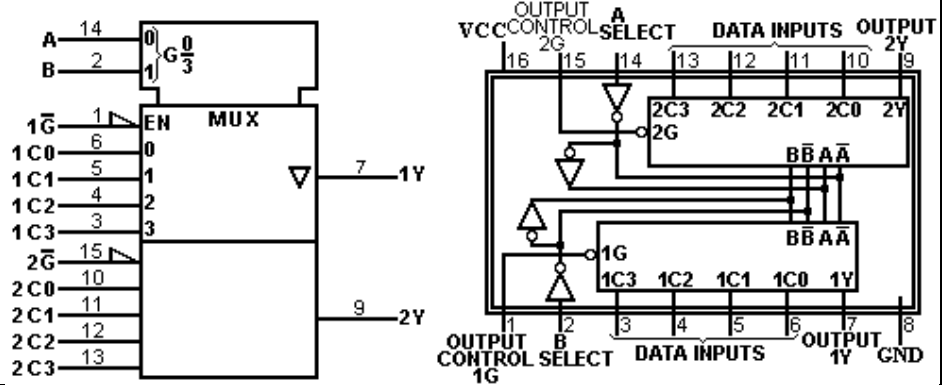


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

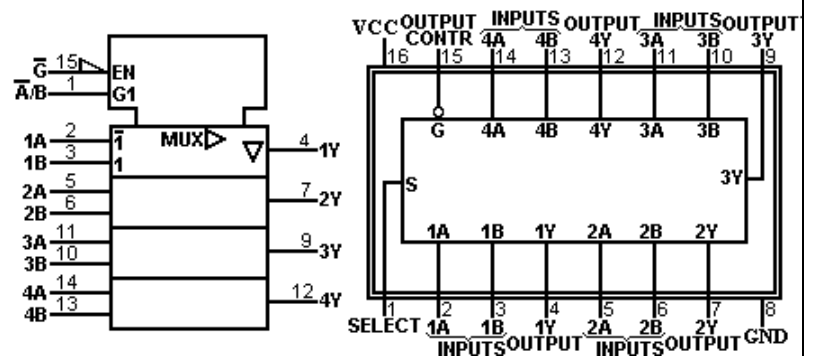


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

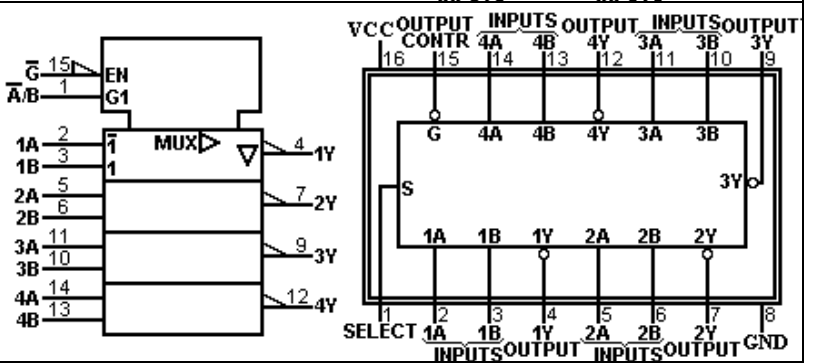
74LS253
Dual Data Selector/Multiplexers



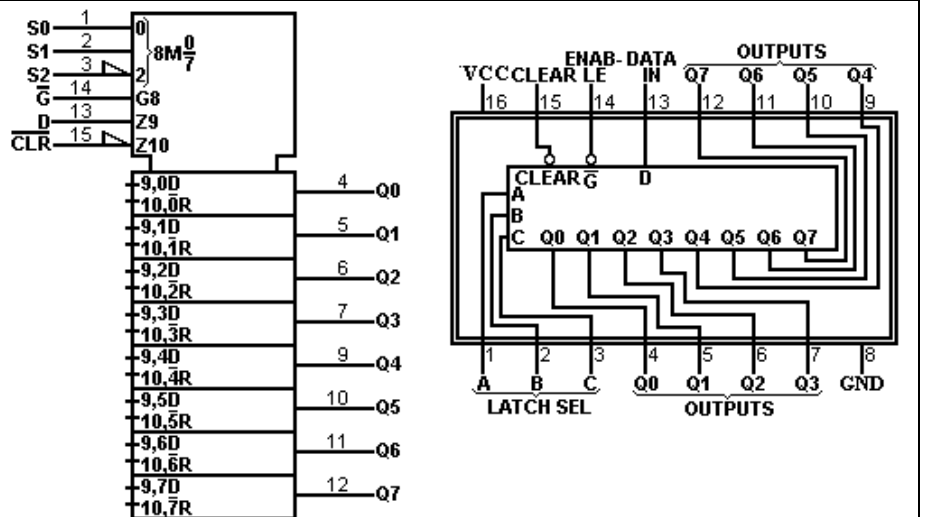
74LS257
Dual Data Selector/Multiplexers



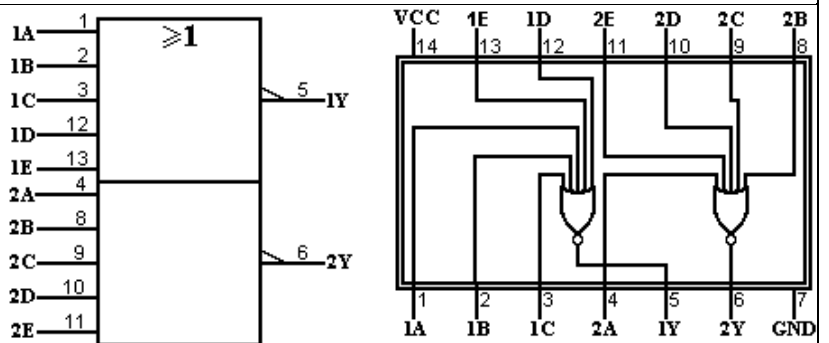
74LS258
Dual Data Selector/Multiplexers



74LS259
Eight-Bit Addressable Latches

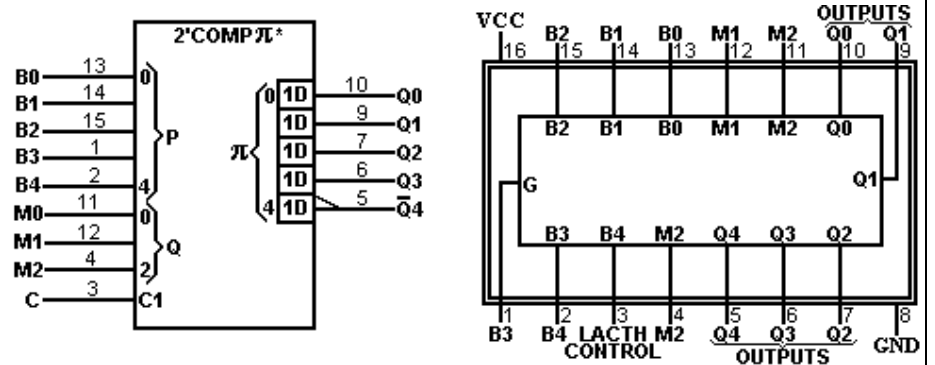


74LS260
Dual 5-Input Positive Nor Gates

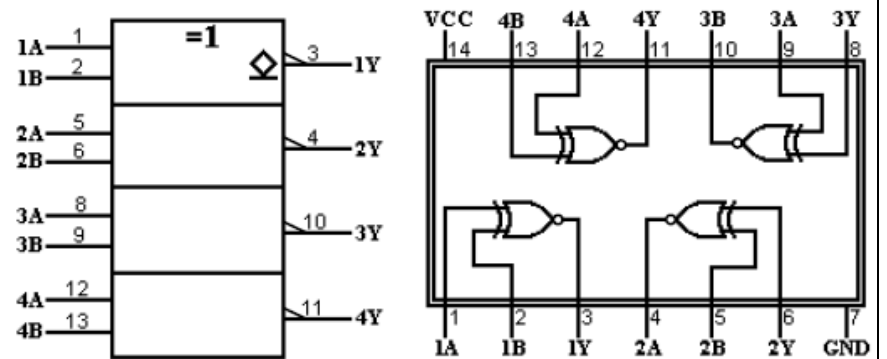


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

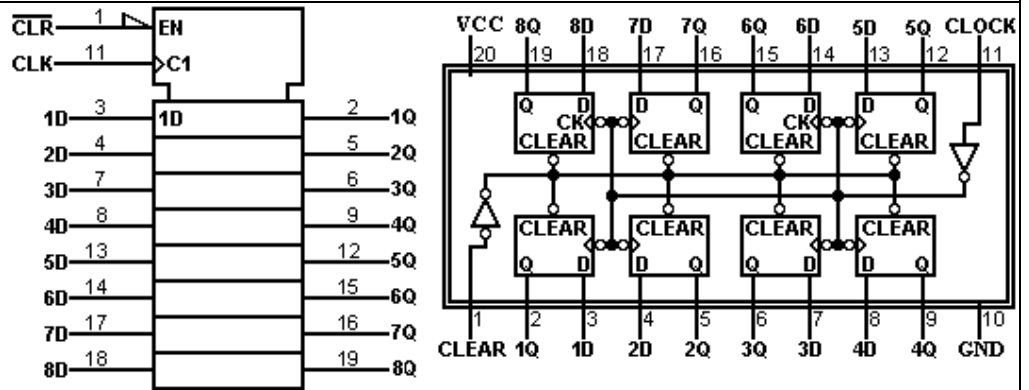
74LS261
2-Bit By 4-Bit
Parallel Binary
Multipliers



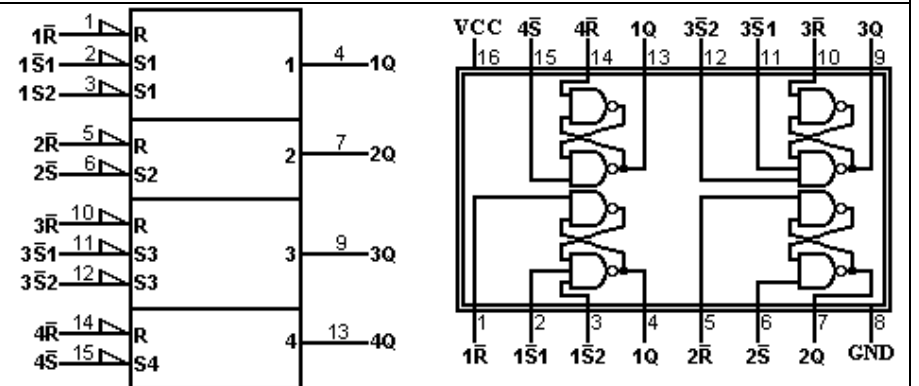
74LS266
Quad 2-Input Exclusive-Nor
Gates
With Open-Collectors Outputs



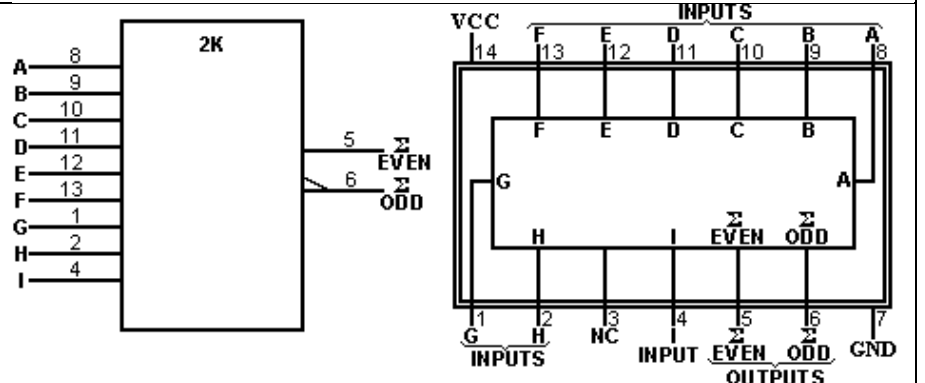
74LS273
Octal D-Type
Flip-Flops



74LS279
Quad \bar{S} - \bar{R} Latches

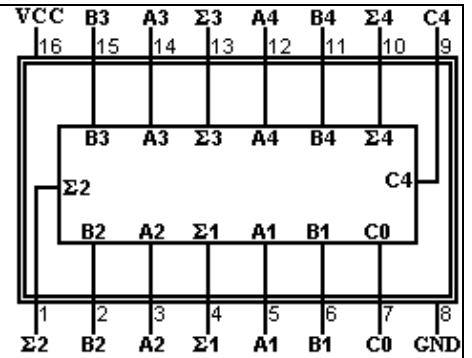
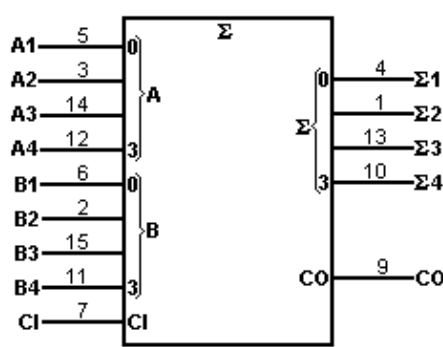


74LS280
9-Bit Odd/Even Parity
Generators/Checkers

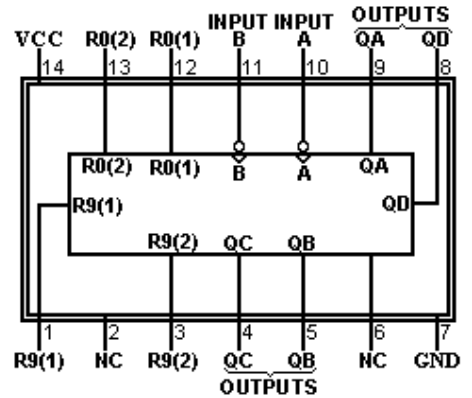
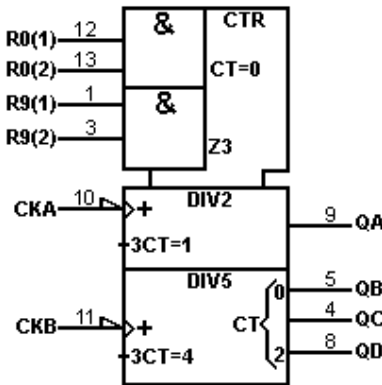


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

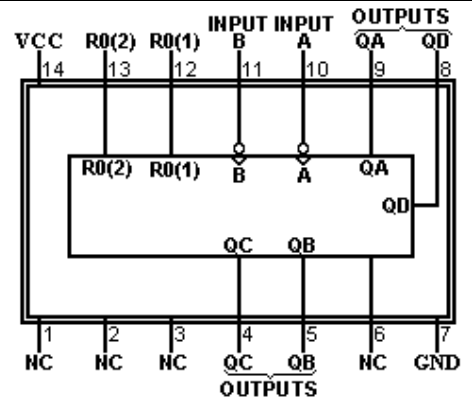
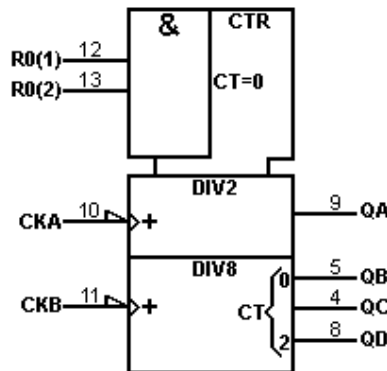
74LS283
4-Bit Binary Full Adders



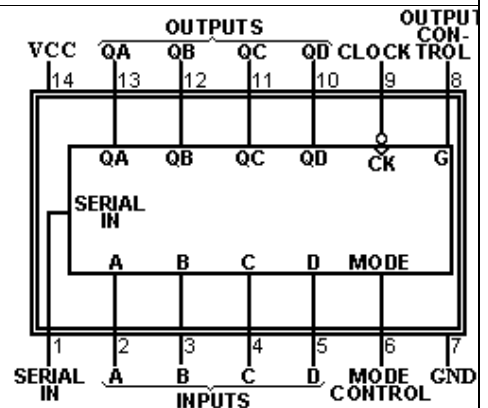
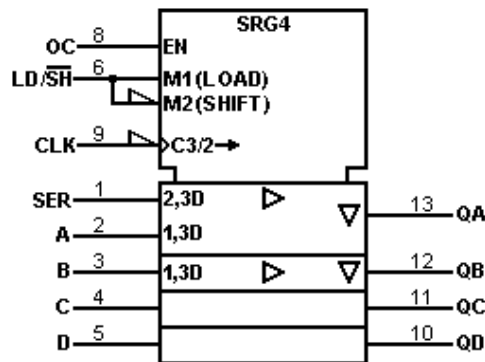
74LS290
Decade Counters



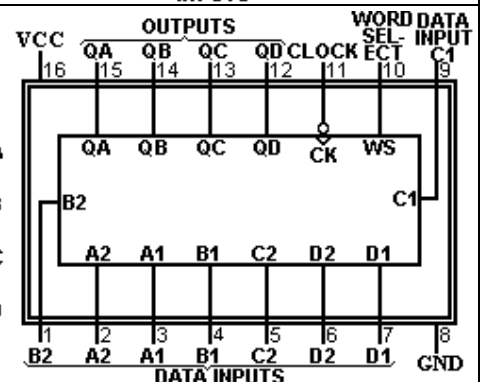
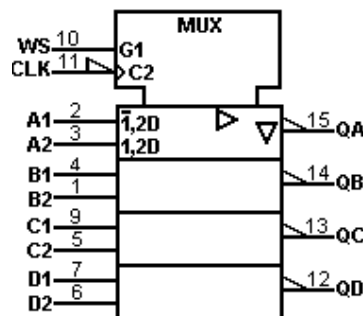
74LS293
4-Bit Binary Counters



74LS295
4-Bit Bidirectional Universal Shift Registers

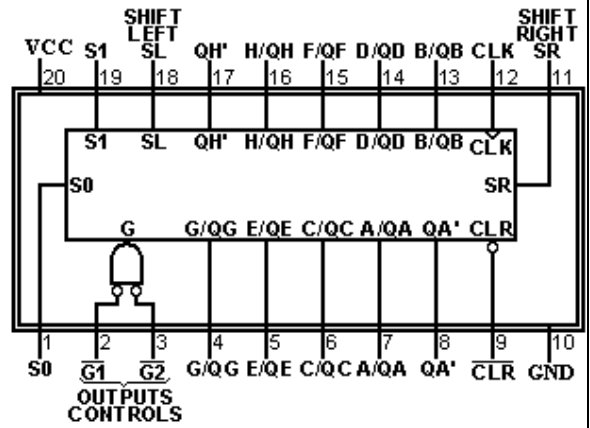
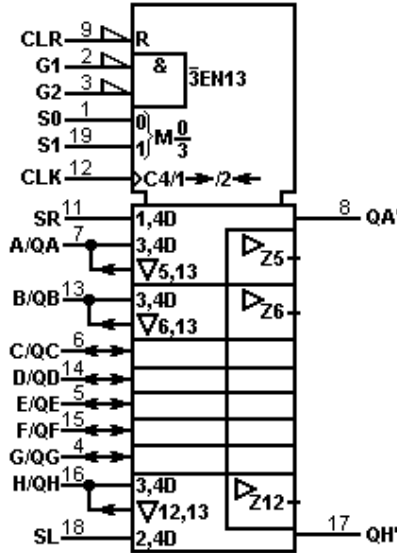


74LS298
Quad 2-Input Multiplexers With Storage

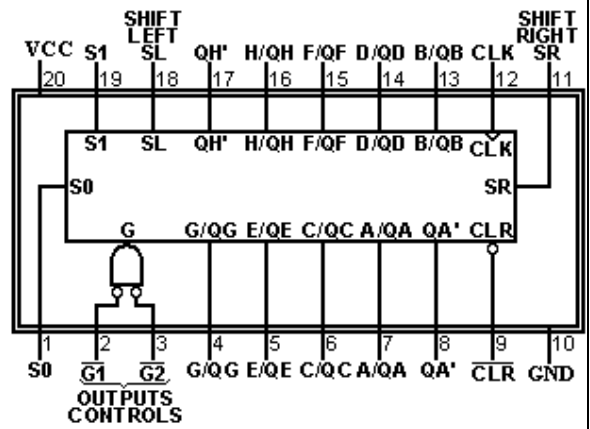
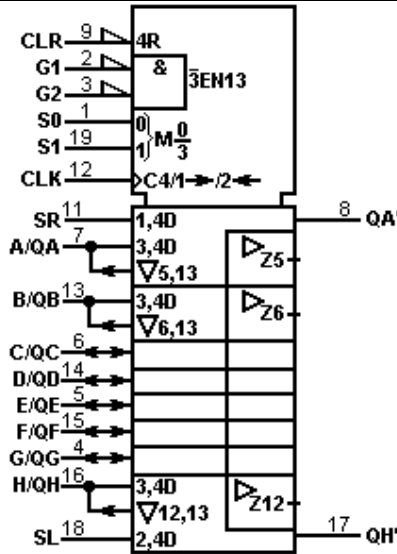


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

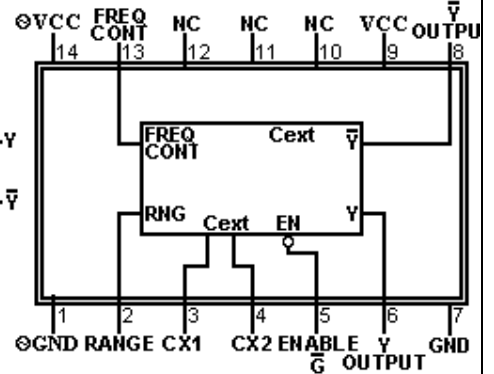
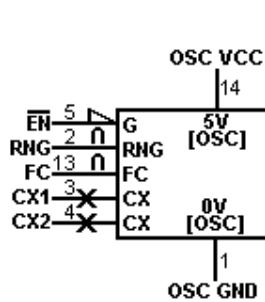
74LS299
8-Bit Bidirectional Universal Shift/Storage Registers



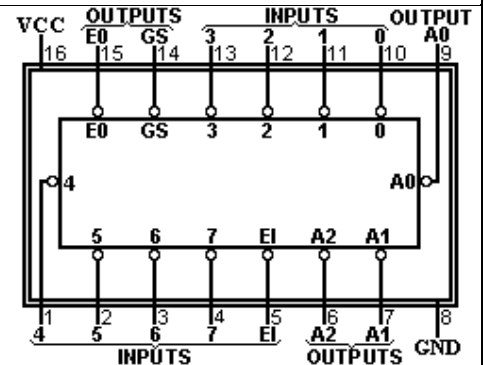
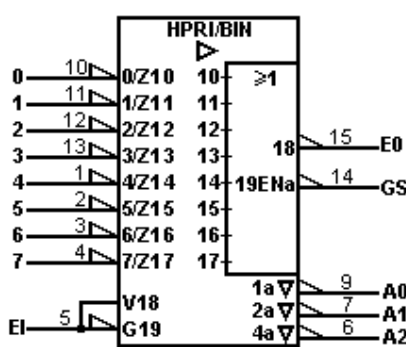
74LS323
8-Bit Bidirectional Universal Shift/Storage Registers



74LS324
Voltage – Controlled Oscillators
Two – Phase Outputs Enable Control

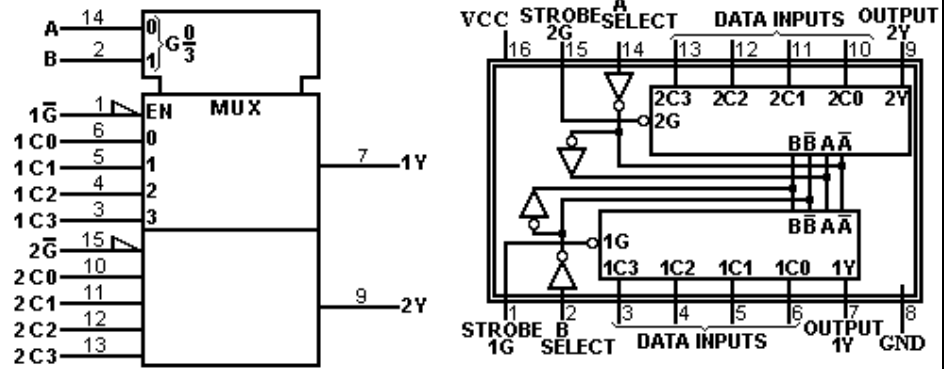


74LS348
8-Line-To-3-Line Priority Encoders

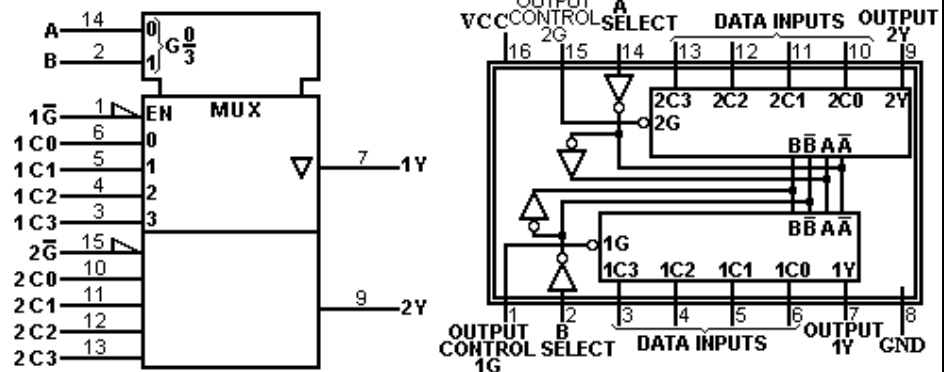


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

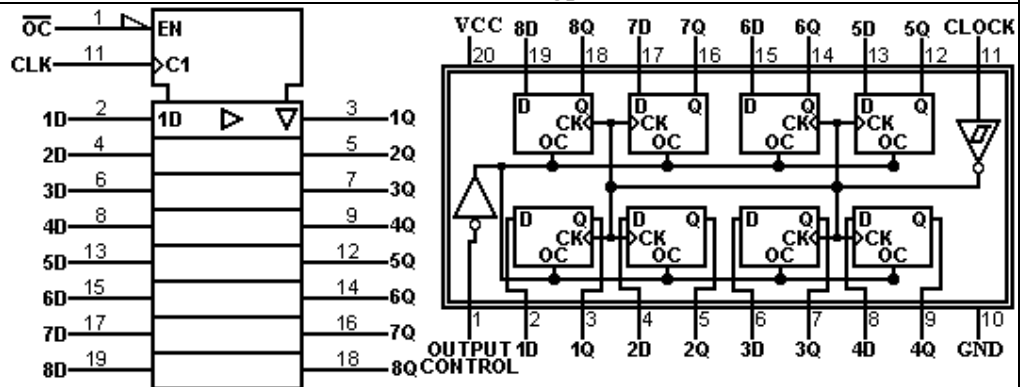
74LS352
Dual 4-Line-To-Line Data Selector/Multiplexers



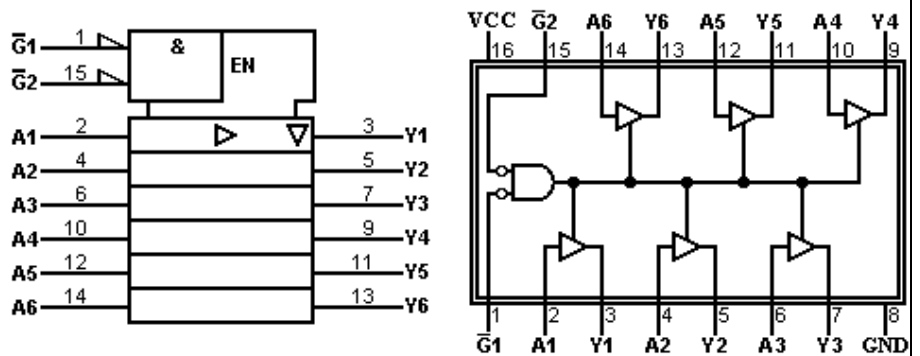
74LS353
Dual 4-Line-To-1-Line Data Selector/Multiplexers



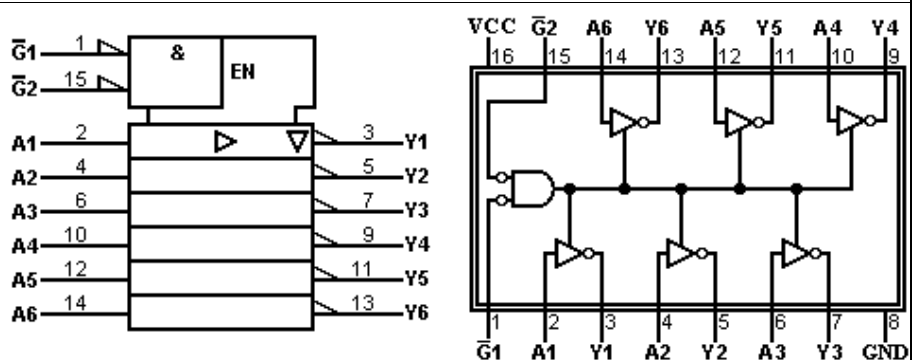
74LS364
Octal D-Type Latches



74LS365
Hex Bus Drivers

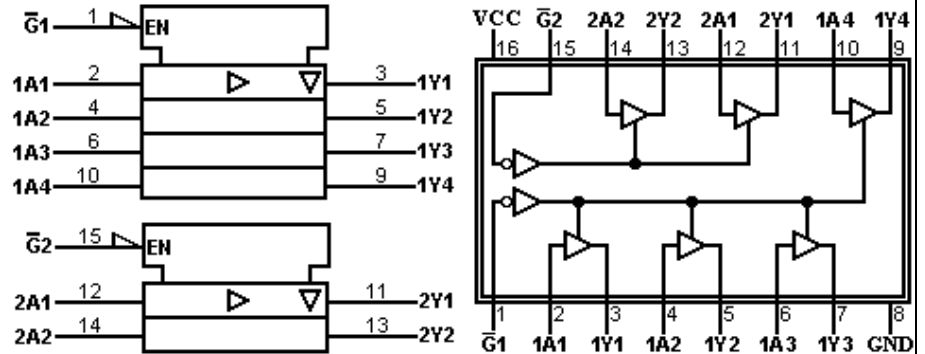


74LS366
Hex Bus Drivers

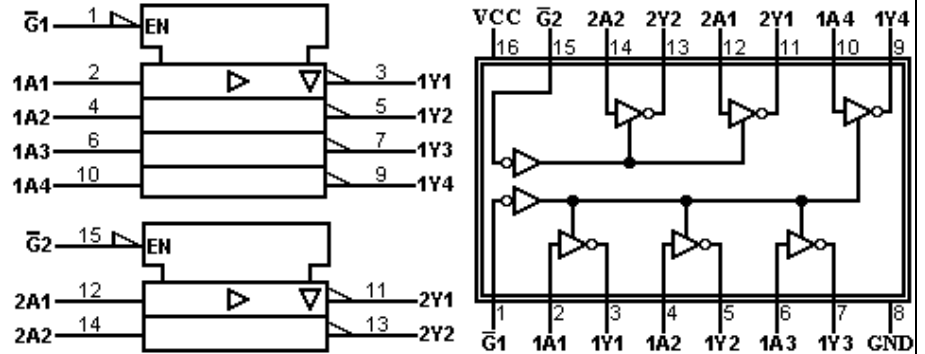


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

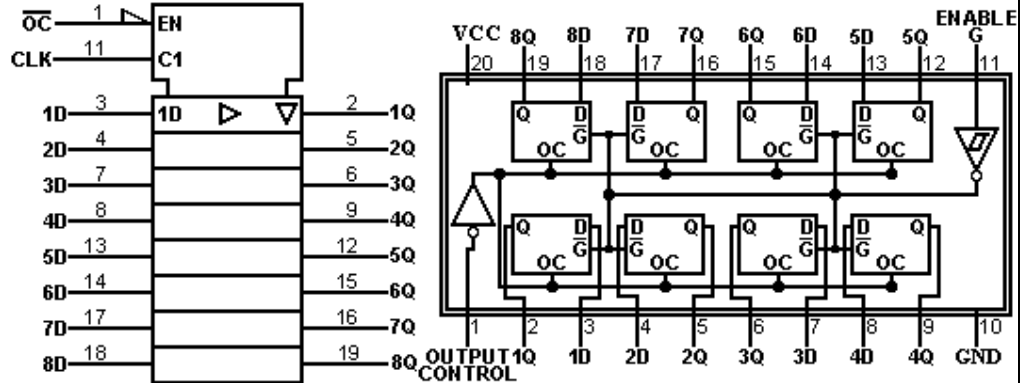
74LS367
Hex Bus Drivers



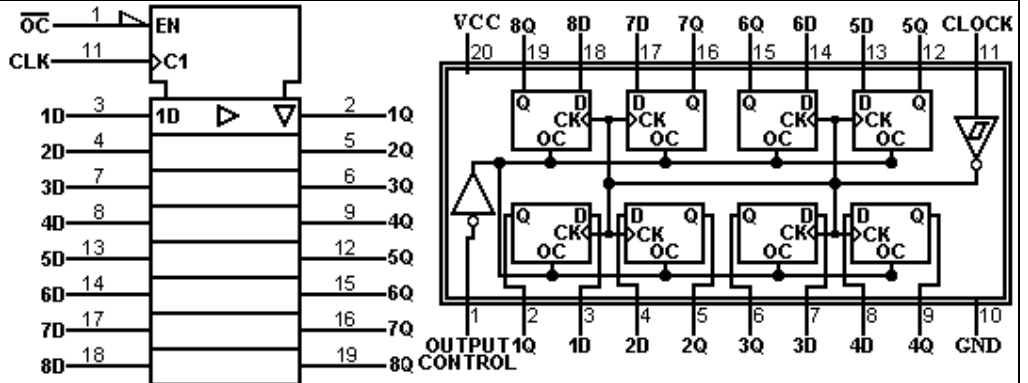
74LS368
Hex Bus Drivers



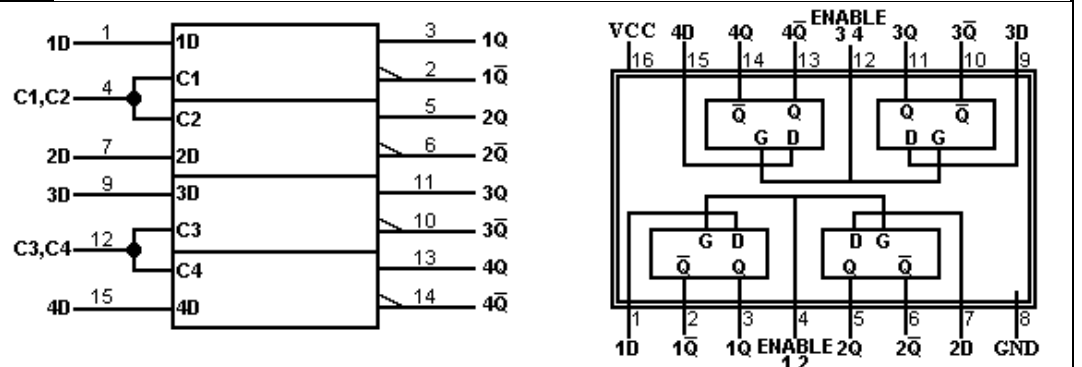
74LS373
Octal D-Type Latches



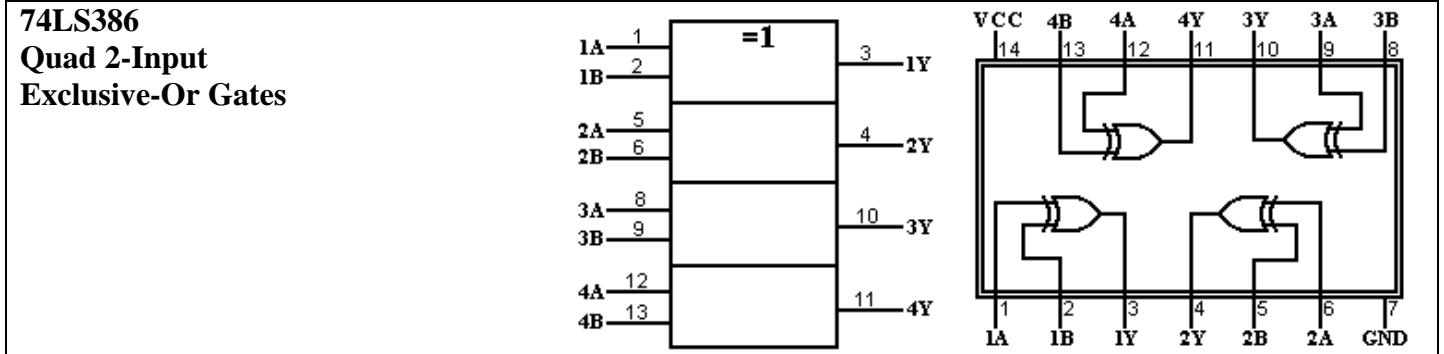
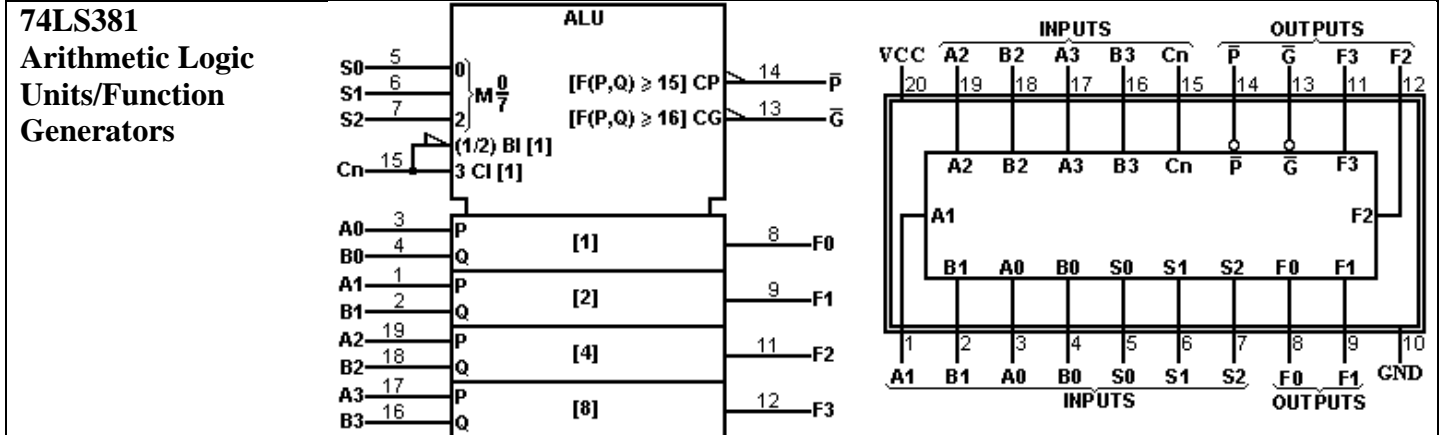
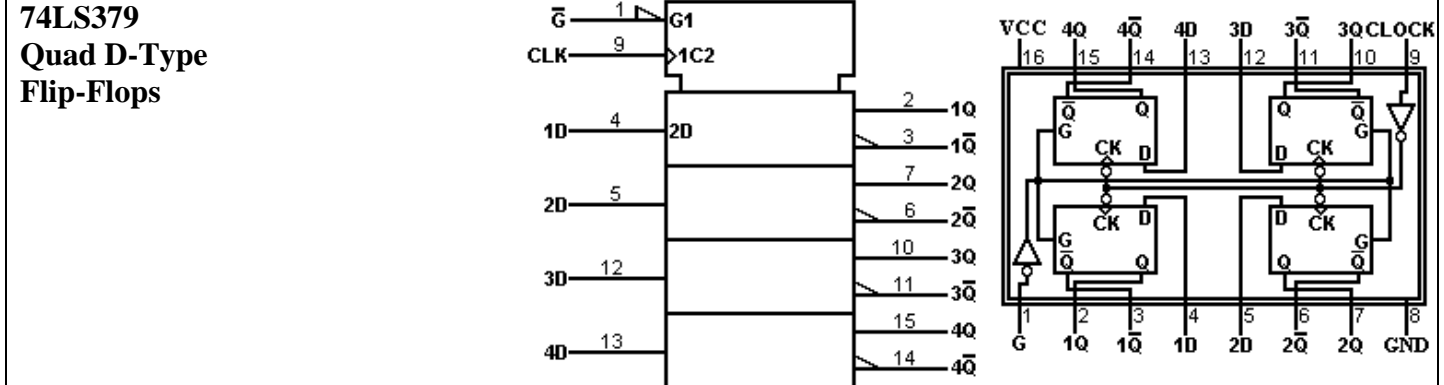
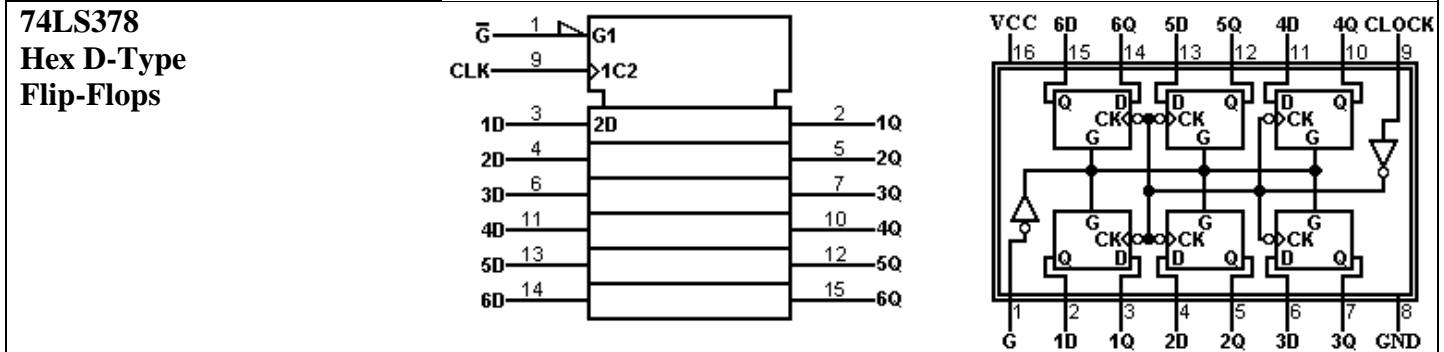
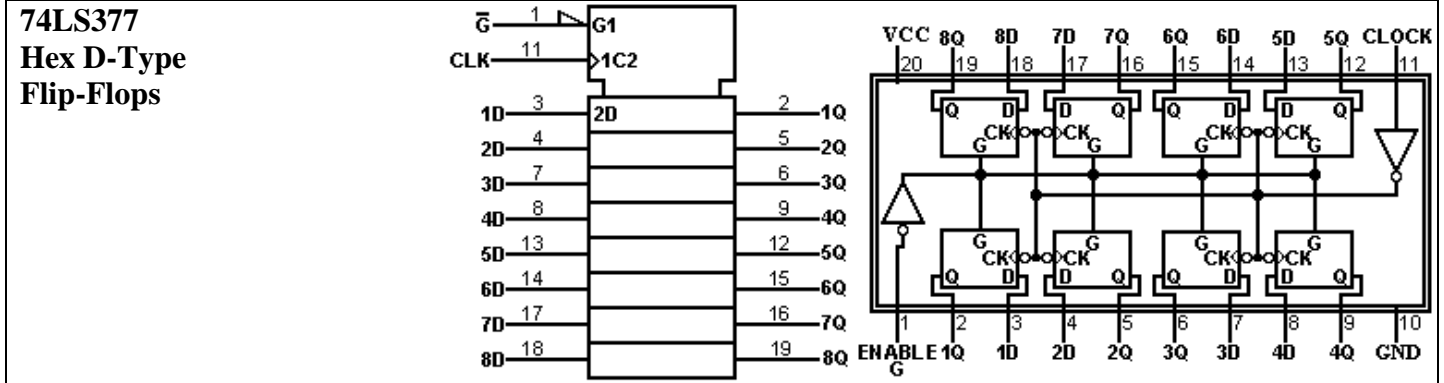
74LS374
Octal D-Type Flip-Flops



74LS375
4-Bit Bistable Latches

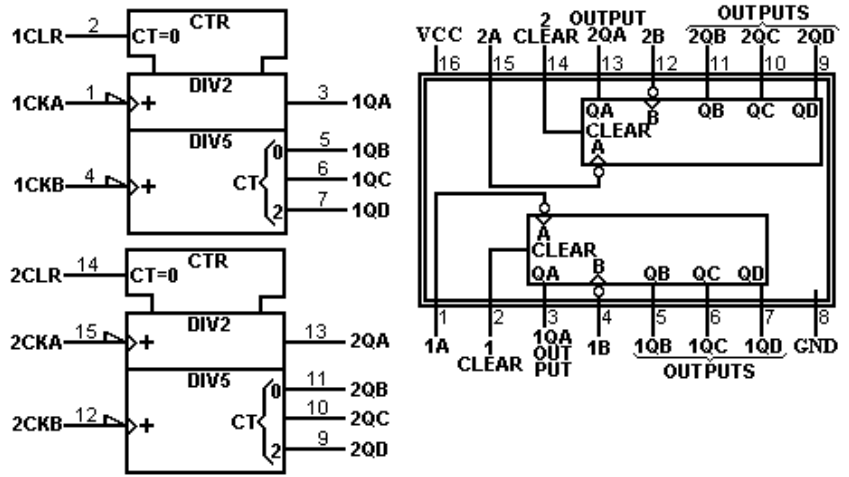


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

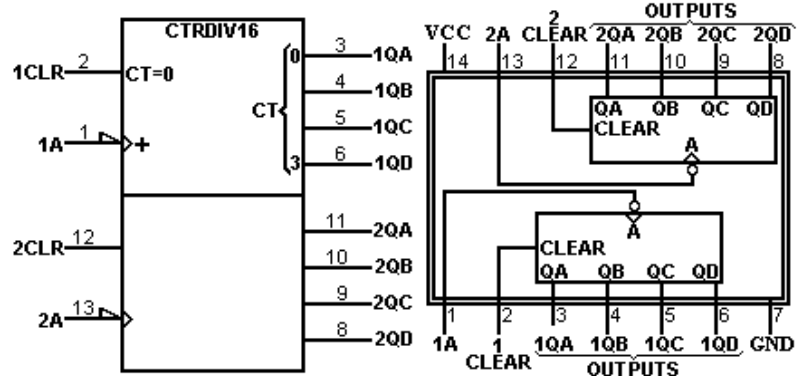


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

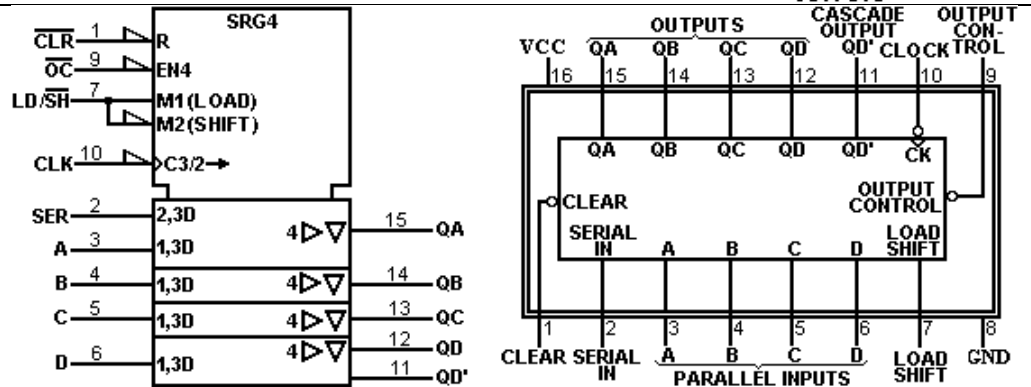
74LS390
Dual Decade Counters



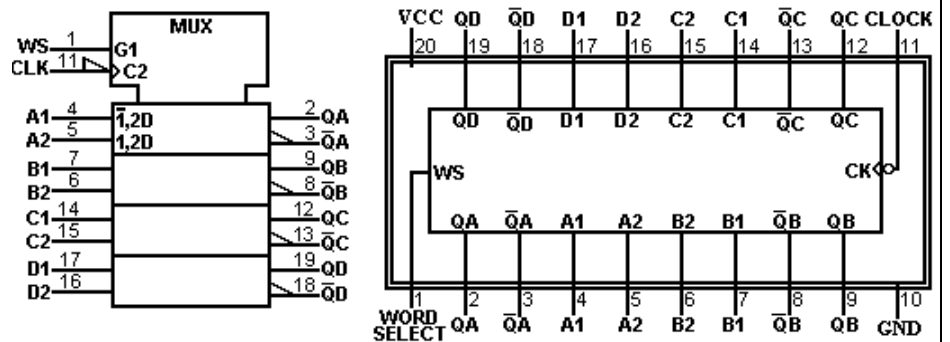
74LS393
Dual 4-Bit Binary Counters



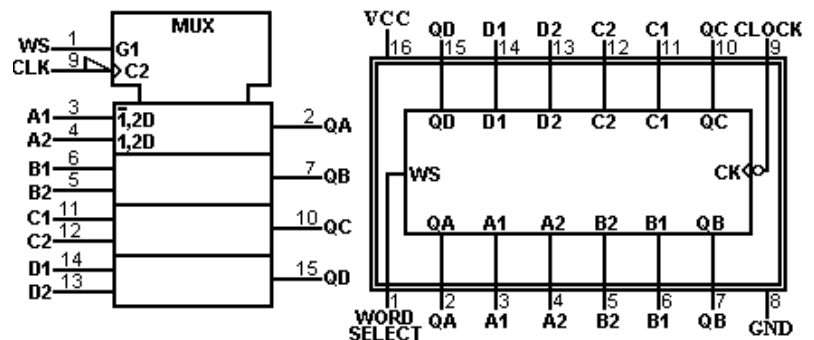
74LS395
4-Bit Universal Shift Registers



74LS398
Quad 2-Input Multiplexers With Storage

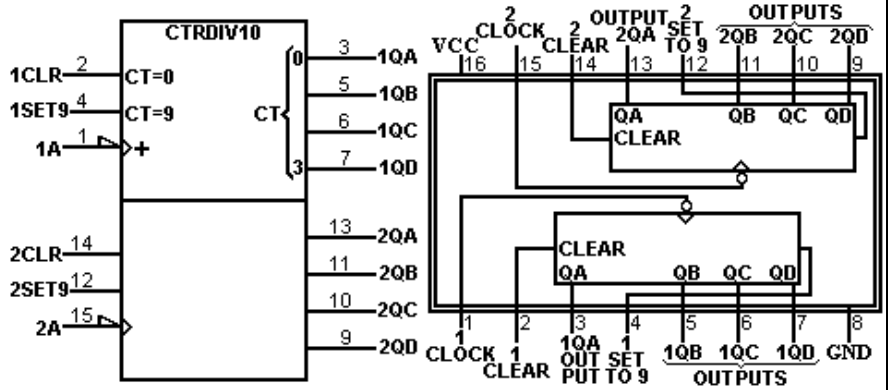


74LS399
Quad 2-Input Multiplexers With Storage

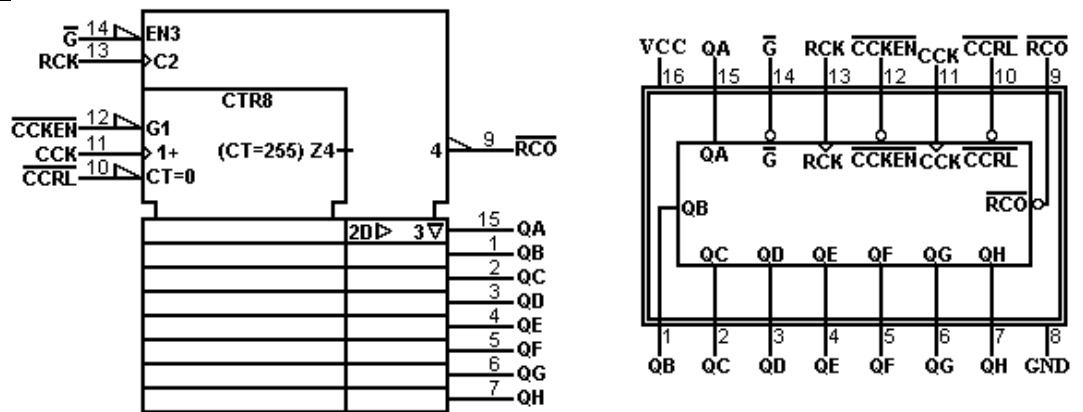


“DATA SHEETS” DE COMPONENTES DA FAMÍLIA LÓGICA TTL

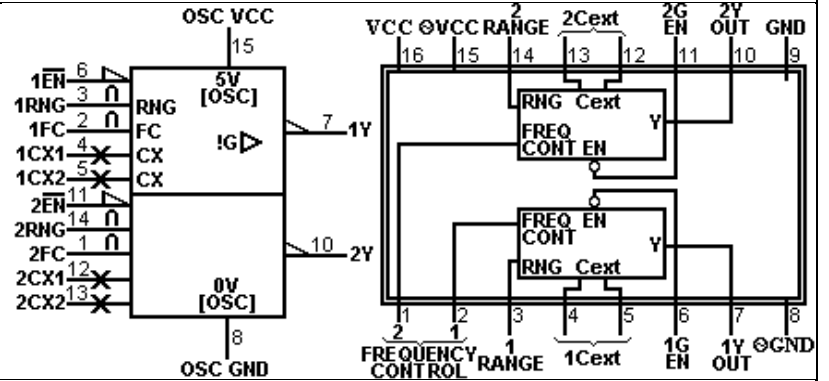
74LS490
Dual Decade Counters



74LS590
8-Bit Binary Counters With Output Registers



74LS629
Voltage-Controlled Oscillators



74LS670
4-By-4 Register Files

